

Dissecting Smart Meters

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- A security services company specializing in helping electric utilities
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- List of services
 - Critical Functionality in Industry Collaboration
 - Security Architecture Guidance and Review
 - Penetration Testing and Security Assessments
 - On the Job and Classroom Training
 - Policy Composition

Who are we to give this Talk?

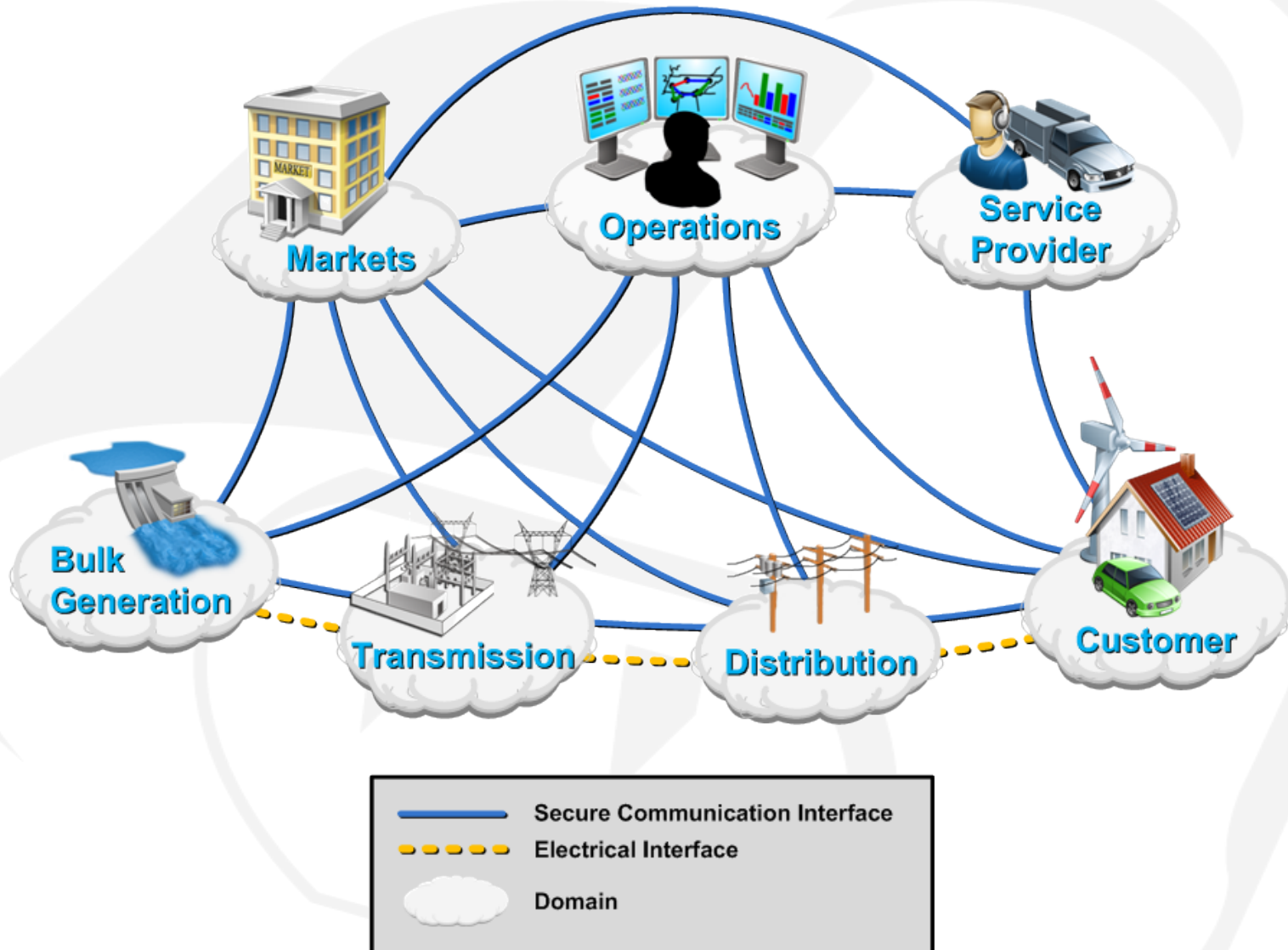


- UtiliSec team has been working with electric utilities, vendors, and Smart Grid community for years
- UtiliSec team has lead and participated in numerous "Smart Grid" security efforts:
 - Facilitates the NERC CIP standards drafting team
 - Served in leadership positions some of the electric utilities largest community groups, including UCAIUG's AMI Sec, Smart Grid Security Working Group, Advancing Security for the Smart Grid (ASAP-SG)
 - Actively contributed to and lead several teams in the creation of NIST Inter-Agency Report 7628: "Guidelines for Smart Grid Cyber Security" (available at: http://csrc.nist.gov/publications/nistir/ir7628/nistir-7628_vol1.pdf, also see vol2 and vol3)
 - Continued participation in DOE's Smart Grid Interoperability Project (SGIP) and new National Electric Sector Cybersecurity Organization (NESCO).

- Many talks have been given on the "Smart Grid"
 - Some accurately and articulately represent the security issues we are dealing with
 - Others over-hype vulnerabilities using outdated, first generation hardware, use fringe vendor products, or simply disable security modes all together
 - Most mistakenly imply that Smart Meters and SCADA are the whole picture
- The media "coverage" always runs with the worst case scenarios, regardless of the messages their interviewees are trying to present
- Lack of clarity in "Smart Grid" security benefits no one
- We need to generate more interest in the security industry to help us secure these systems
 - There is more than enough work to go around

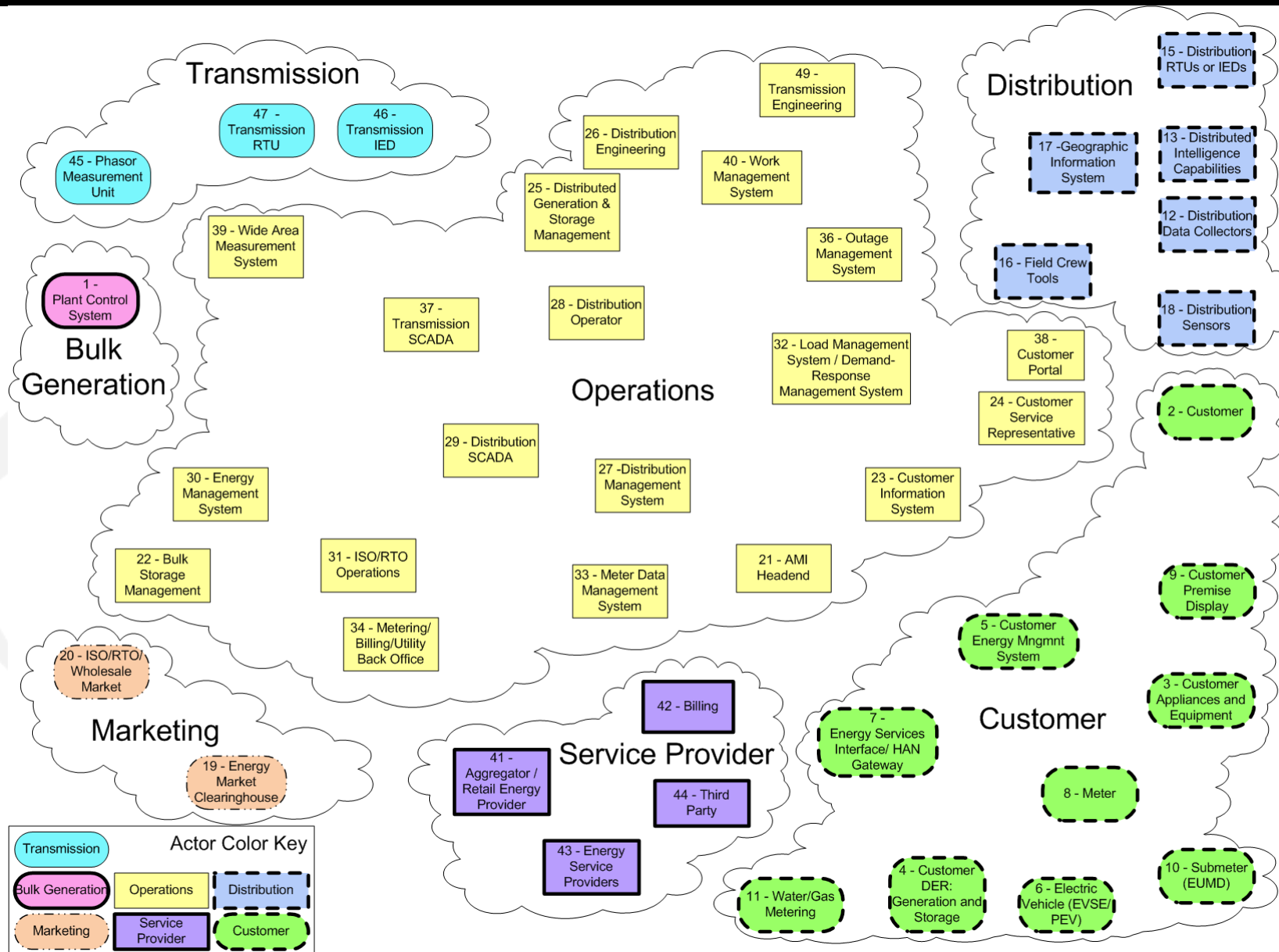
Architectural Overview of the Smart Grid

What is the "Smart Grid"?

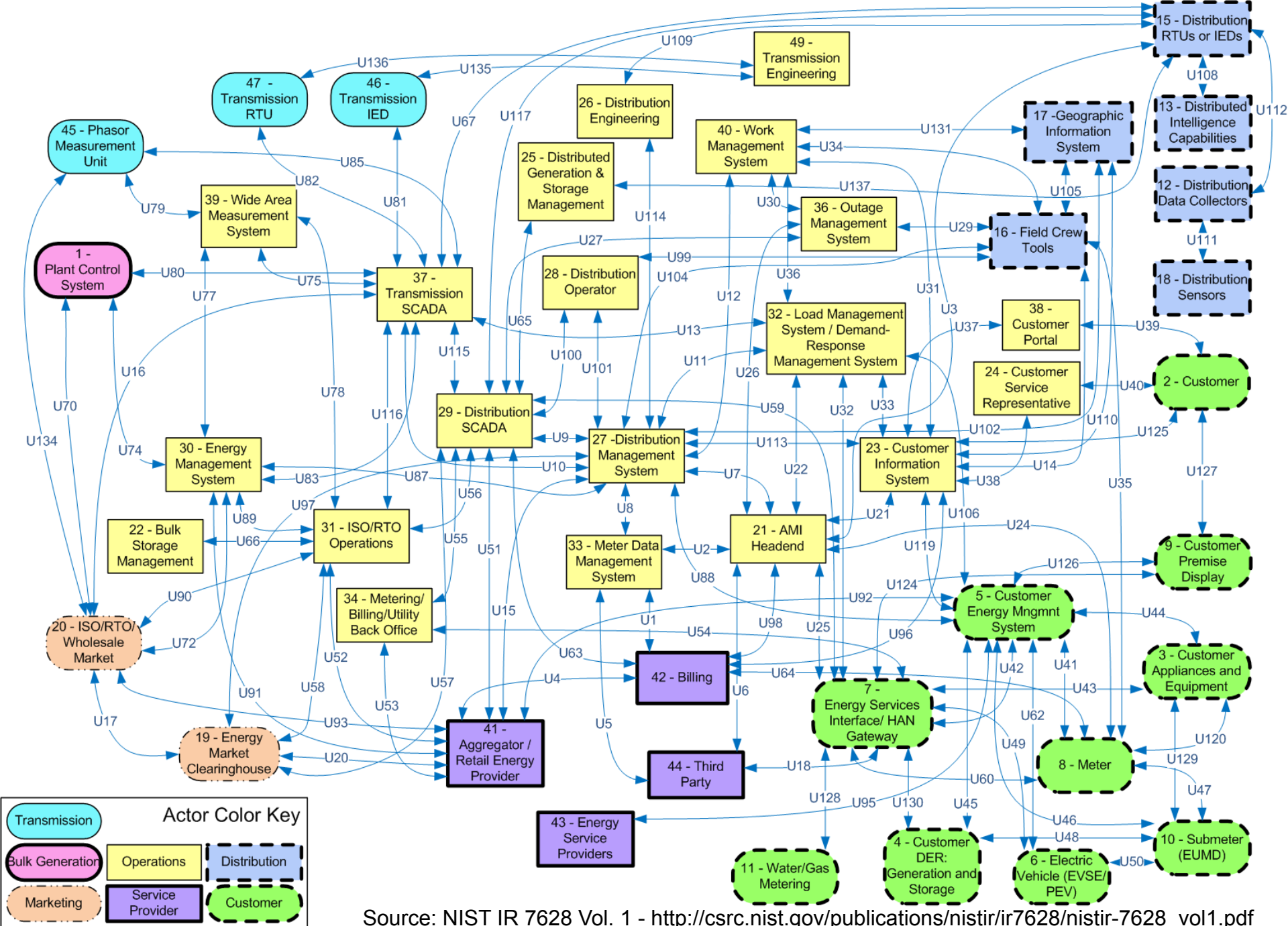


Source: <http://www.sgiclearinghouse.org/ConceptualModel>

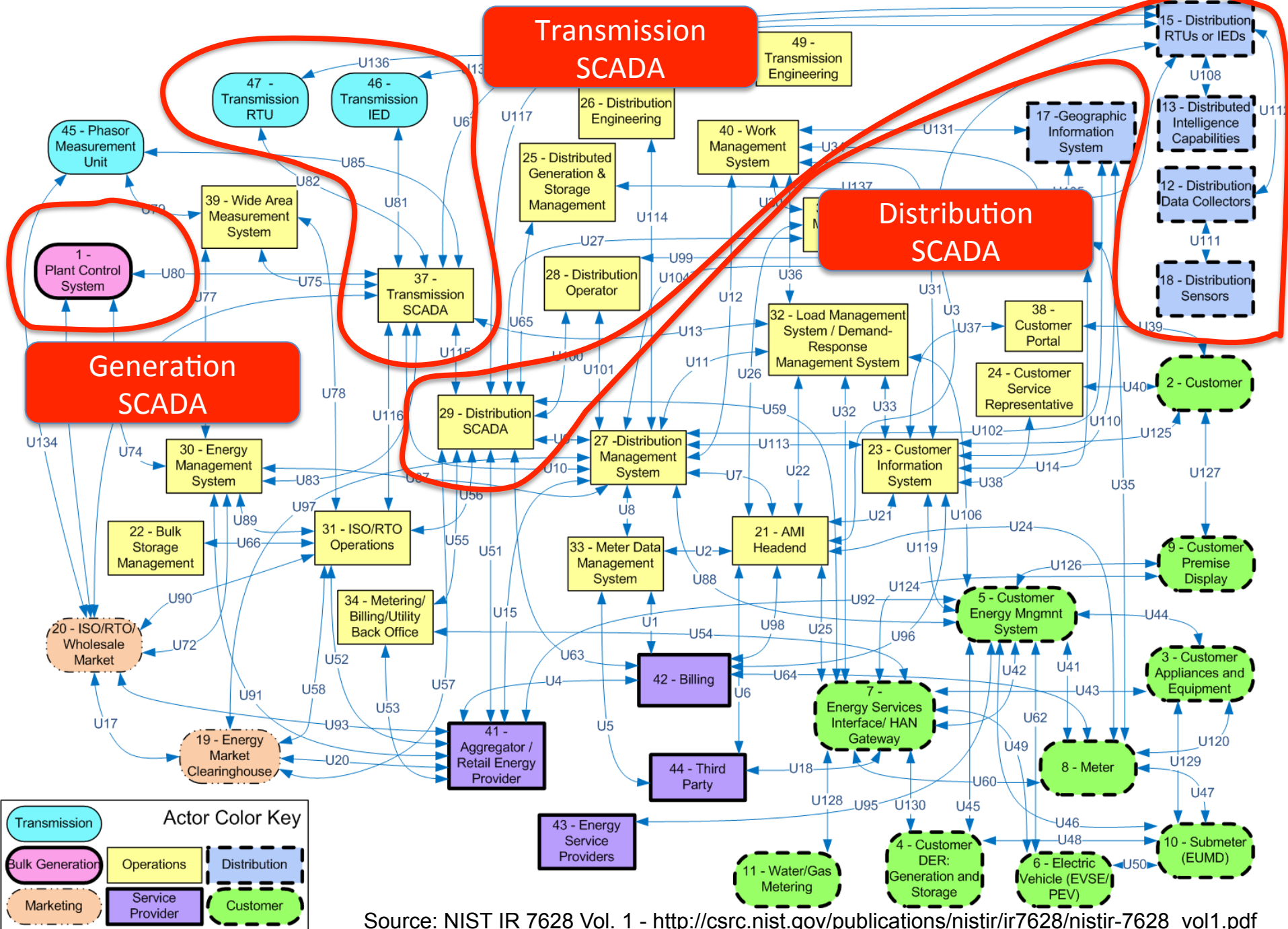
NIST Smart Grid Reference Model



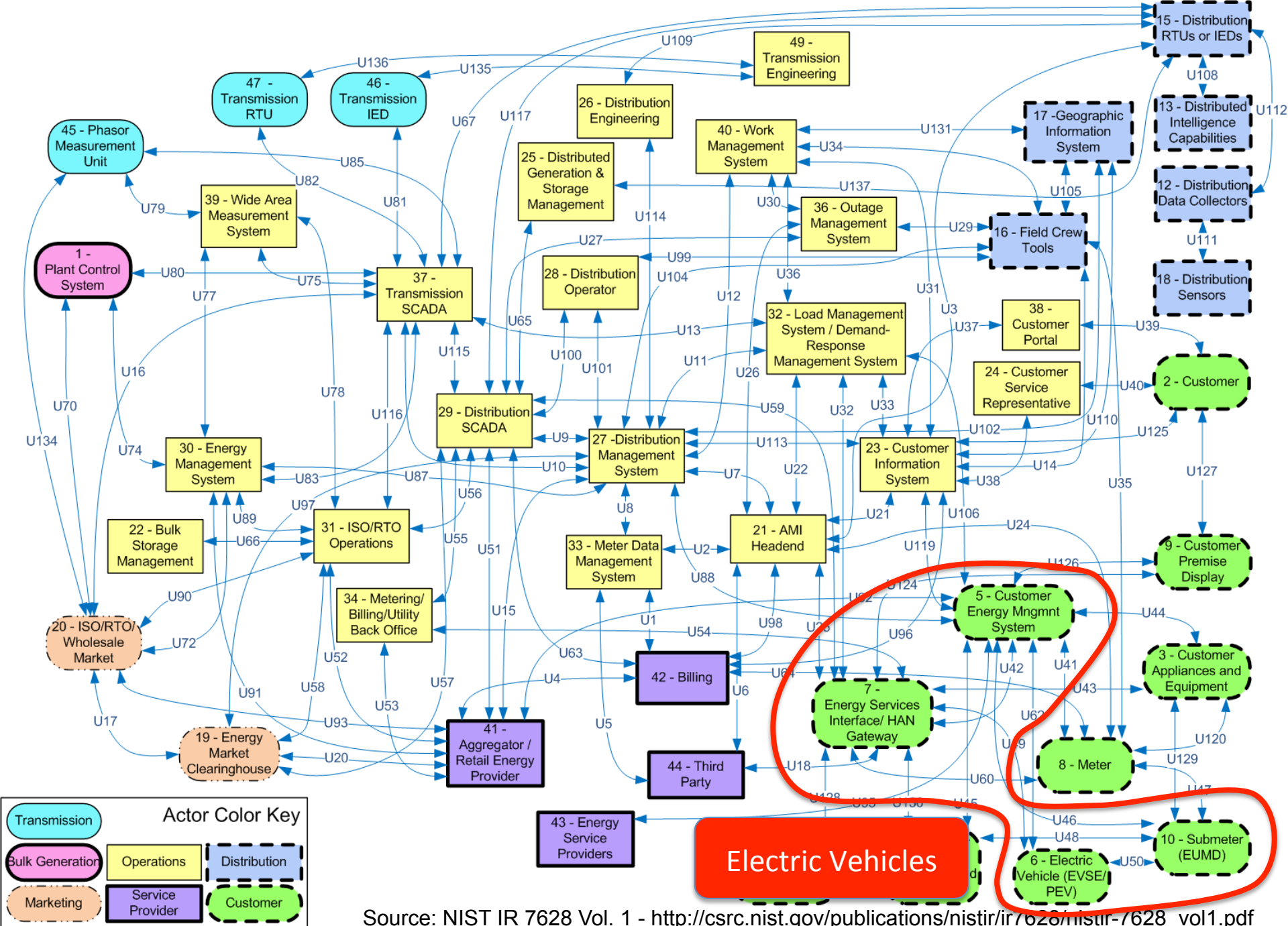
Source: NIST IR 7628 Vol. 1
http://csrc.nist.gov/publications/nistir/7628/nistir-7628_vol1.pdf



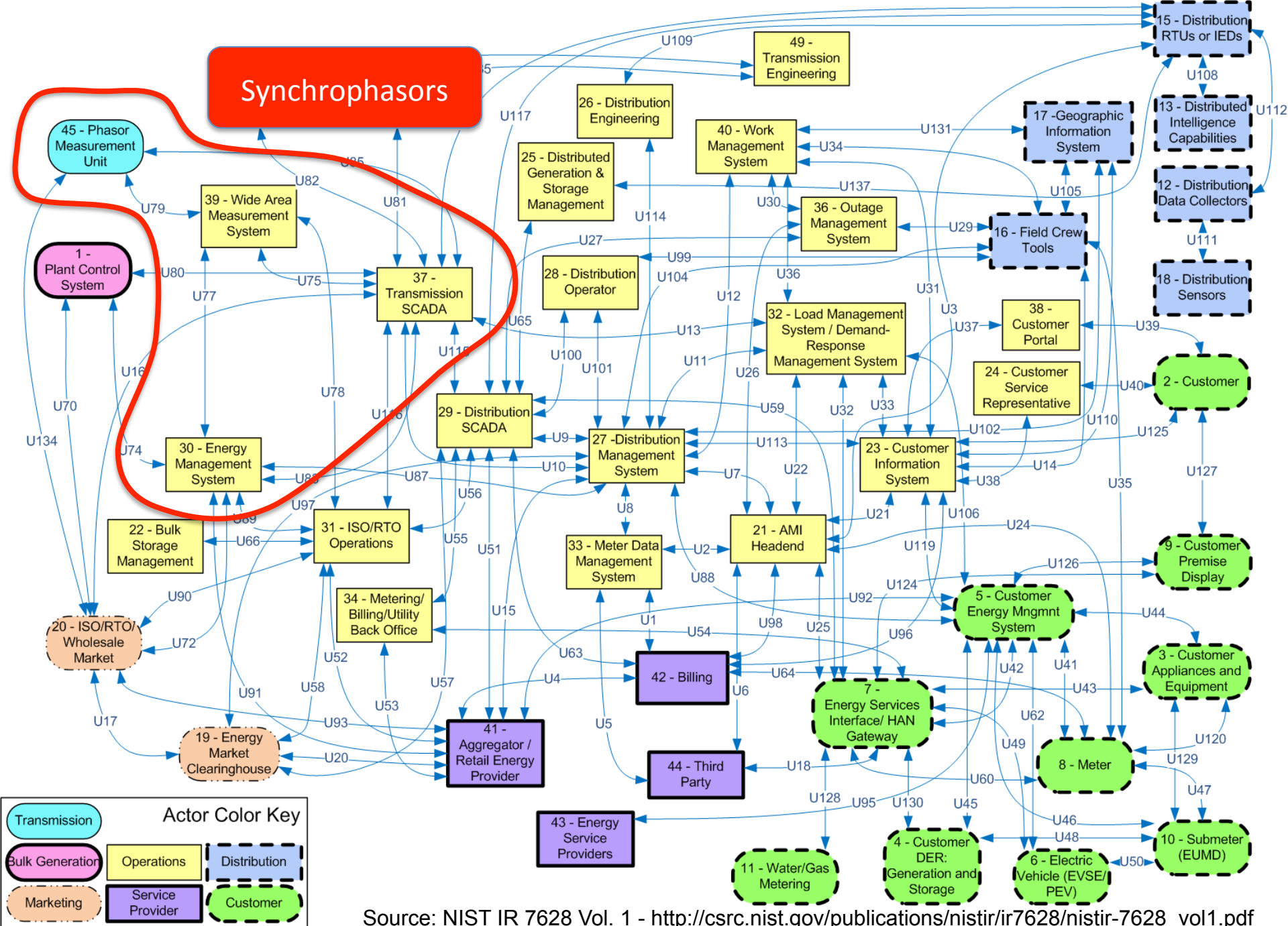
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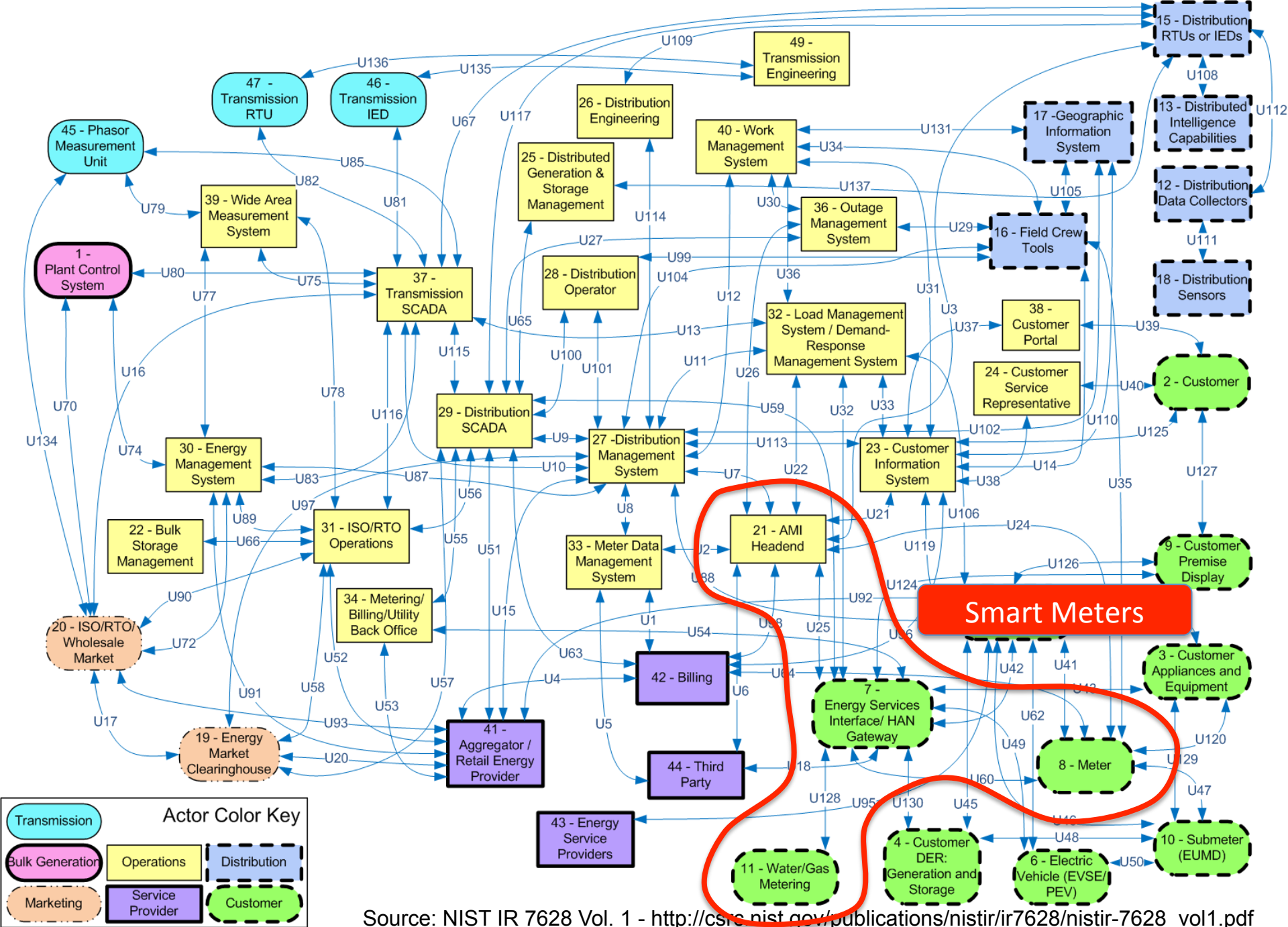
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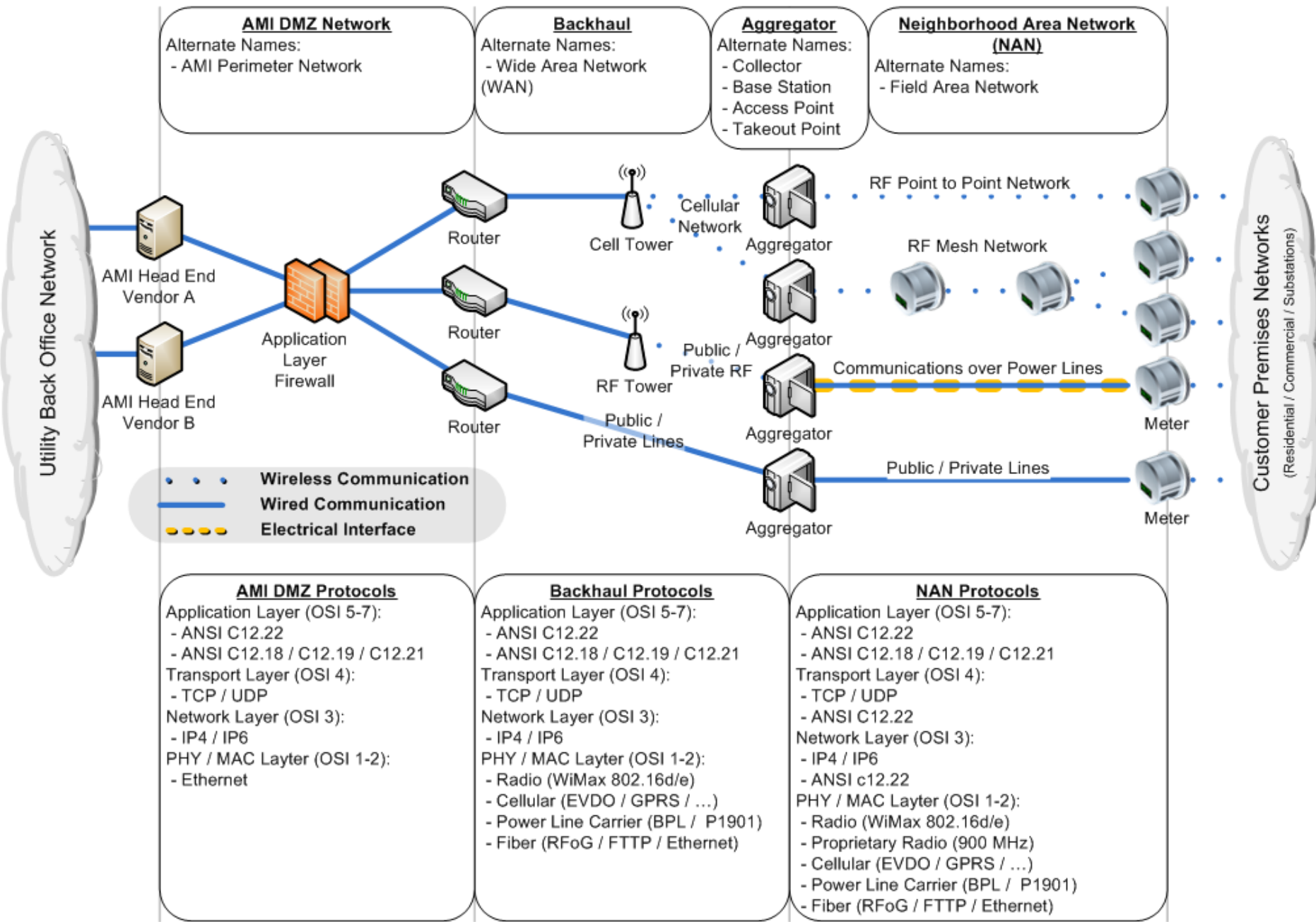


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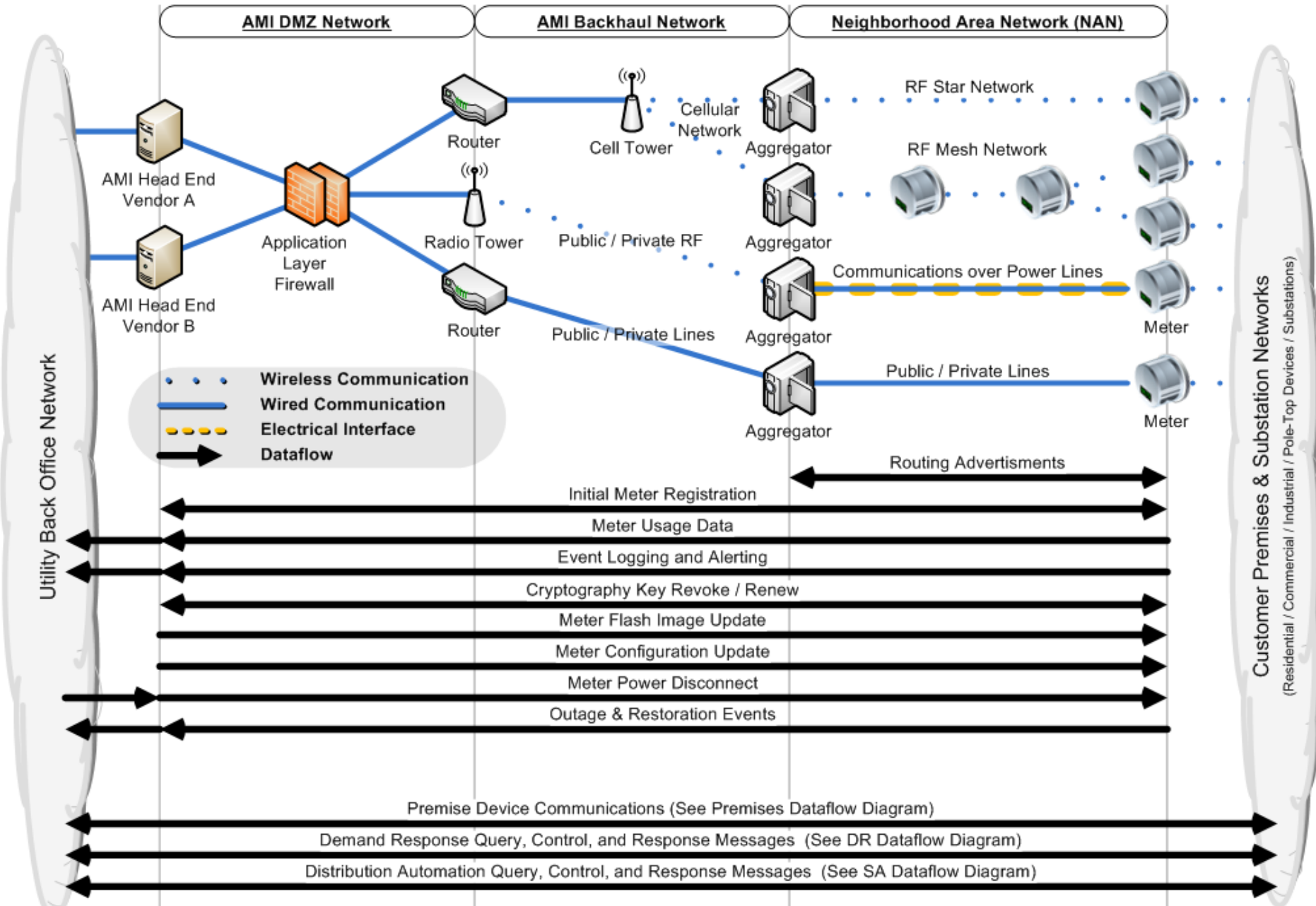


Source: NIST IR 7628 Vol. 1 - http://csrc.nist.gov/publications/nistir/ir7628/nistir-7628_vol1.pdf

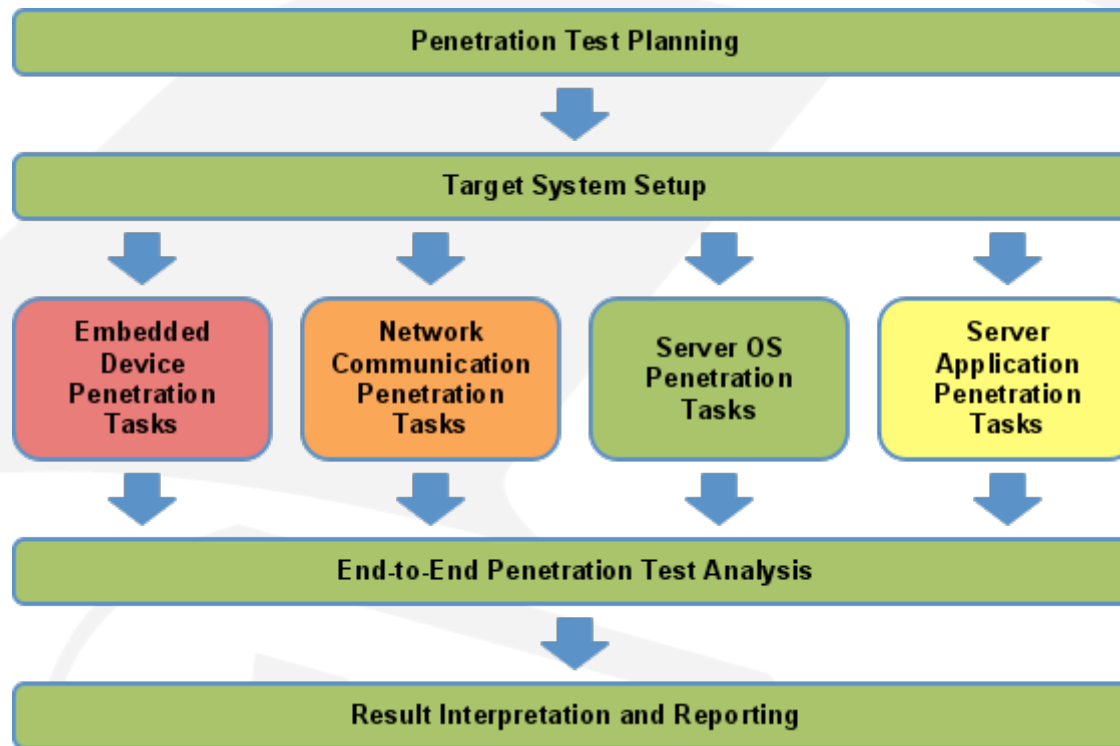
AMI Network Diagram



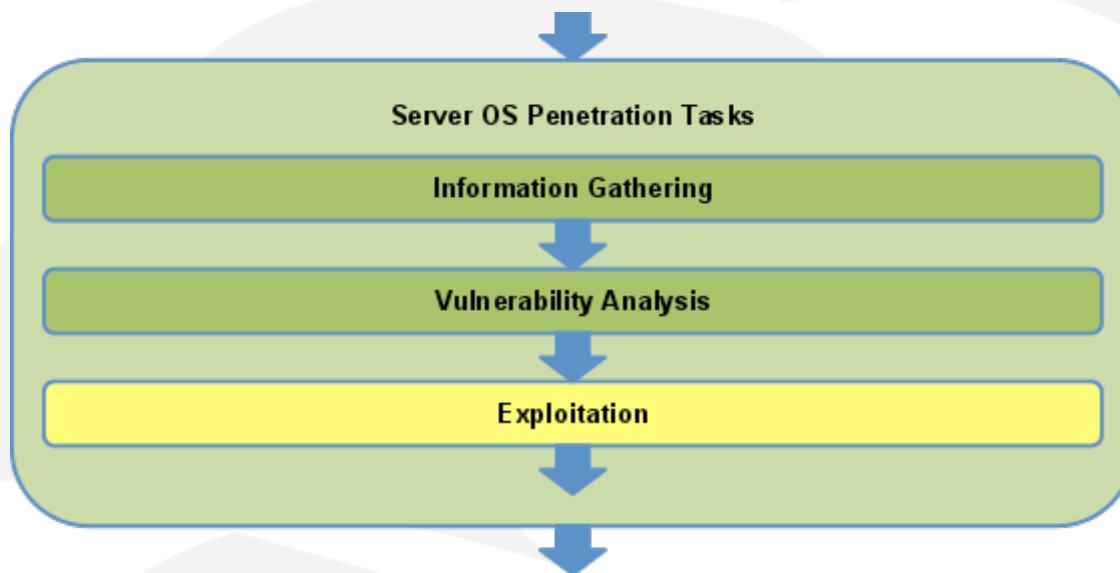
AMI Dataflow Diagram



Penetration Testing Methodology

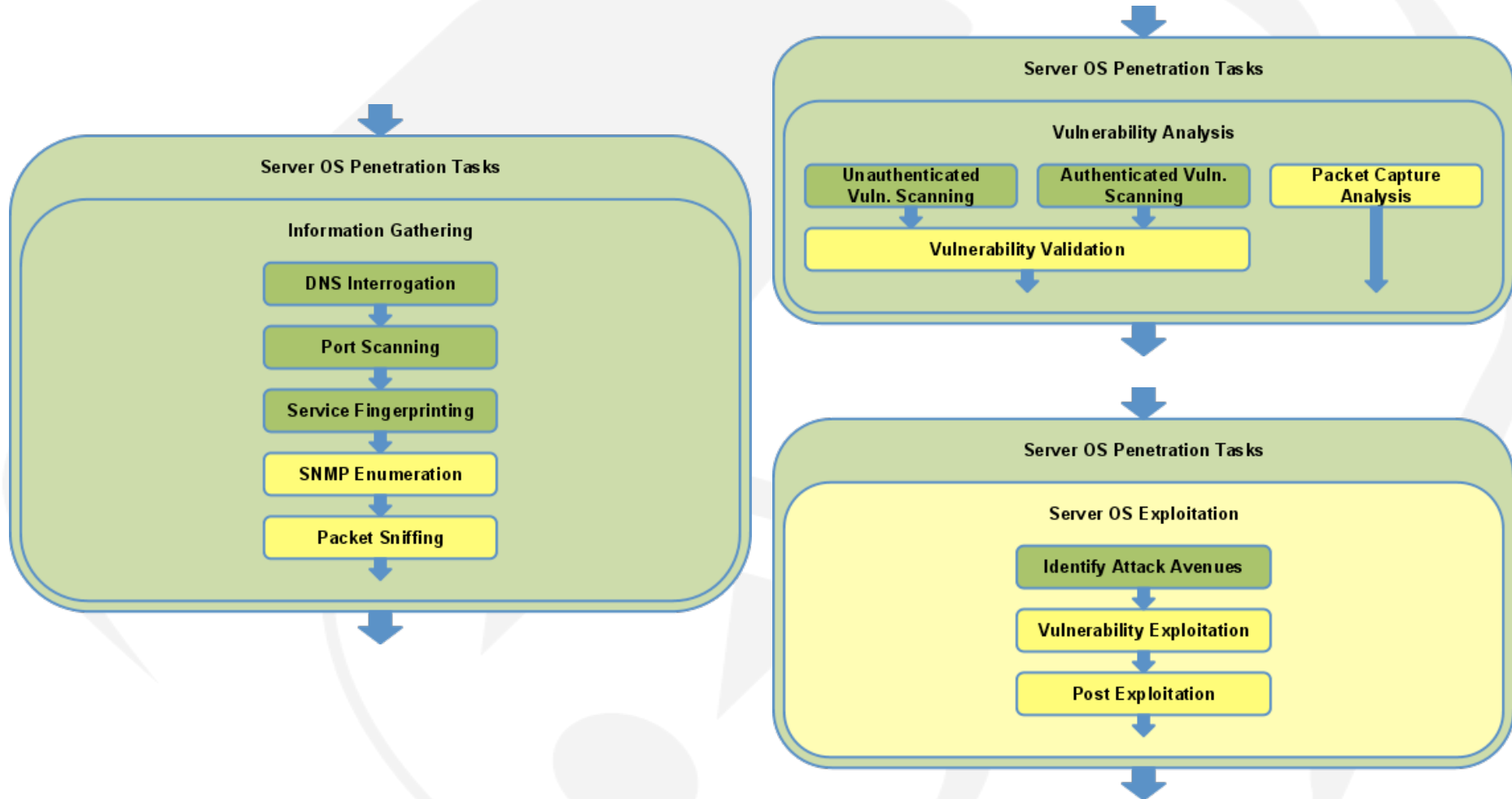


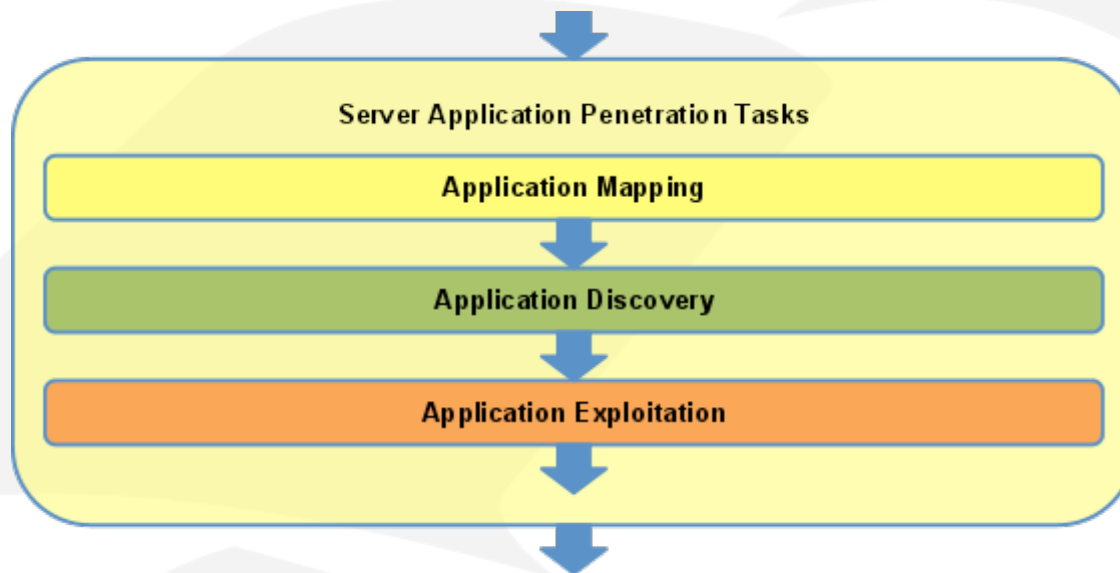
- Green: Tasks most frequently and require the most basic of penetration testing skill
- Yellow: Tasks commonly performed and require moderate penetration testing skill
- Orange: Tasks that are occasionally performed but require higher levels of expertise
- Red: Tasks performed infrequently and require highly specialized skills



- Most servers in the datacenter controlling Smart Grid systems are running commodity OSes like Windows and Linux
- Skills needed to pentest these systems are no different than non-smart grid pentests
- Level of care when testing production systems is greatly increased
- Mastery and understanding of automated tools used is critical

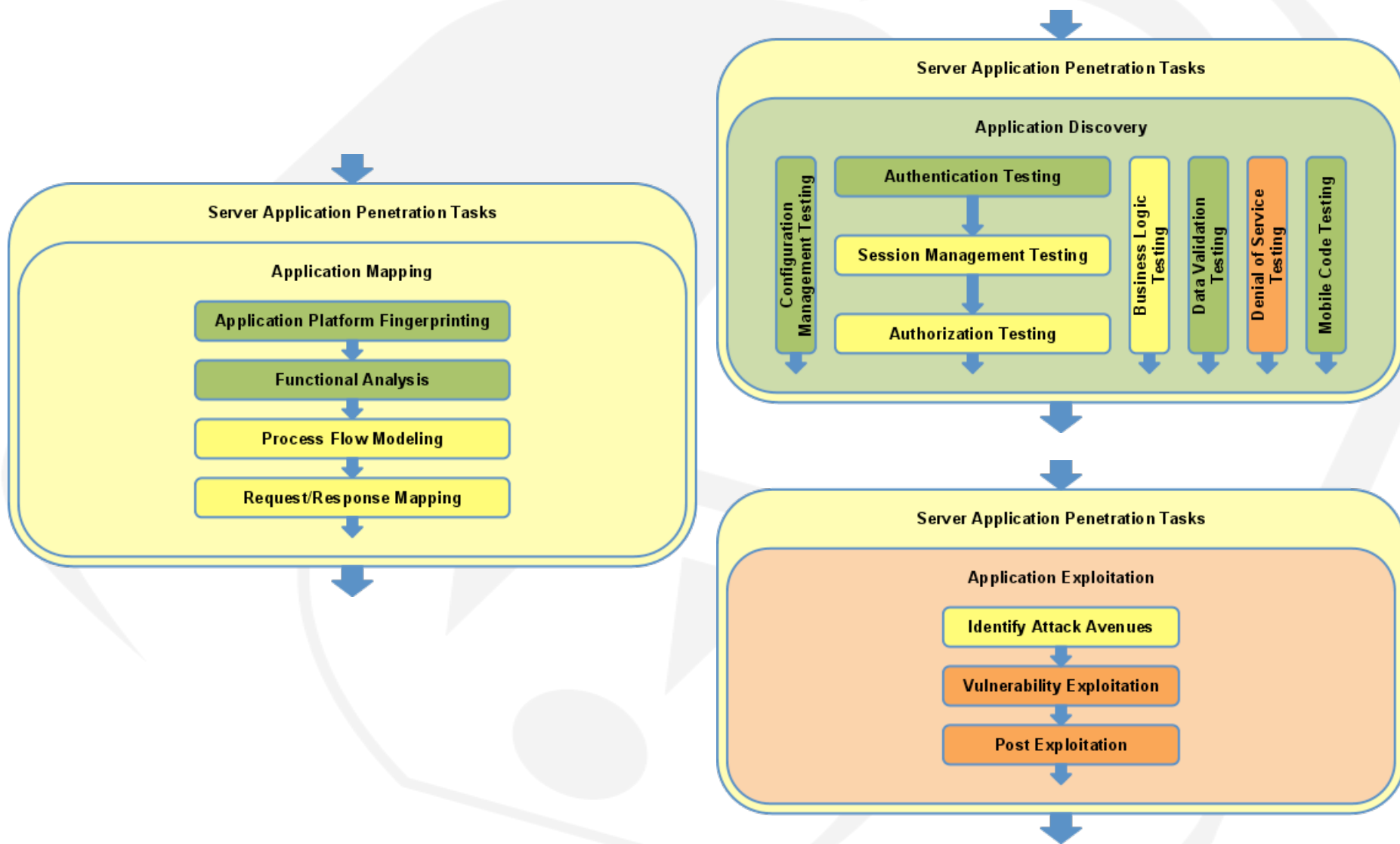
Server OS Pentest Tasks



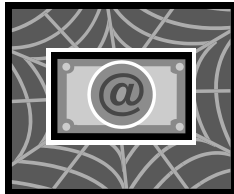


- Includes all user interfaces and smart grid services
- Most modern user interfaces are web applications or fat applications speaking to a web service
- Most server-to-server communications use SOAP or REST web services, but other interfaces like RPC are occasionally seen
- Automated tools can be VERY dangerous in these applications as POST requests can shut down power or brick field devices

Server App Pentest Tasks



Task: Session Management (CSRF) UtiliSec



Attacker Controlled Site

Attack Prerequisites

- Attacker must have knowledge of the application he is attacking (can be obtained at conferences)
- Attacker must know the hostname or IP address of the CIS system (can be obtained by browser based attacks)

3

Hidden in the page, the Attacker's website tells the employee's web browser to disconnect a customer's power

2

Employee opens a second tab and surfs to the Attacker website (or MySpace page...)

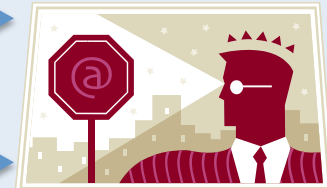
Utility Network

1

Employee using CIS system throughout the day

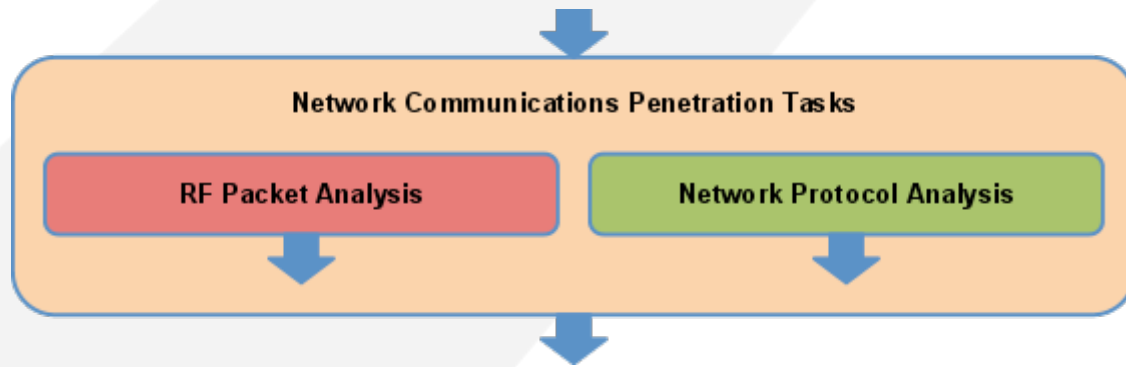
4

Web browser sends disconnect request to CIS



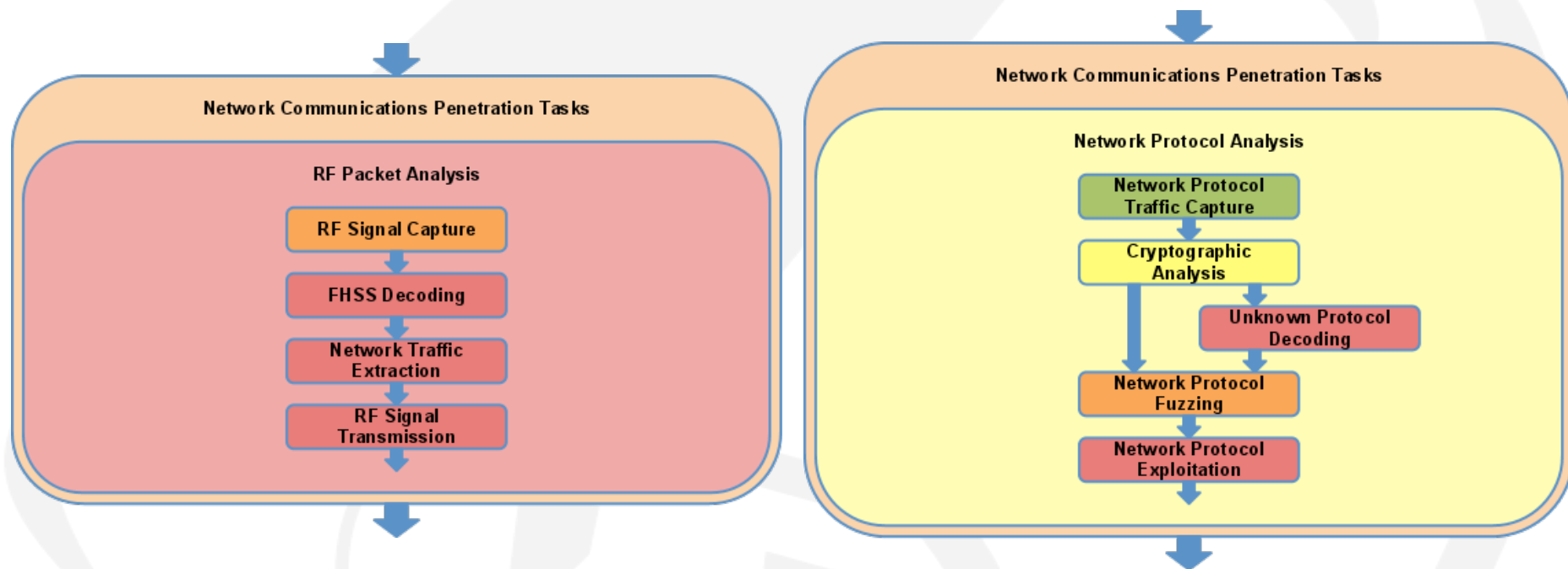
Customer Information System with Power Disconnect Capabilities



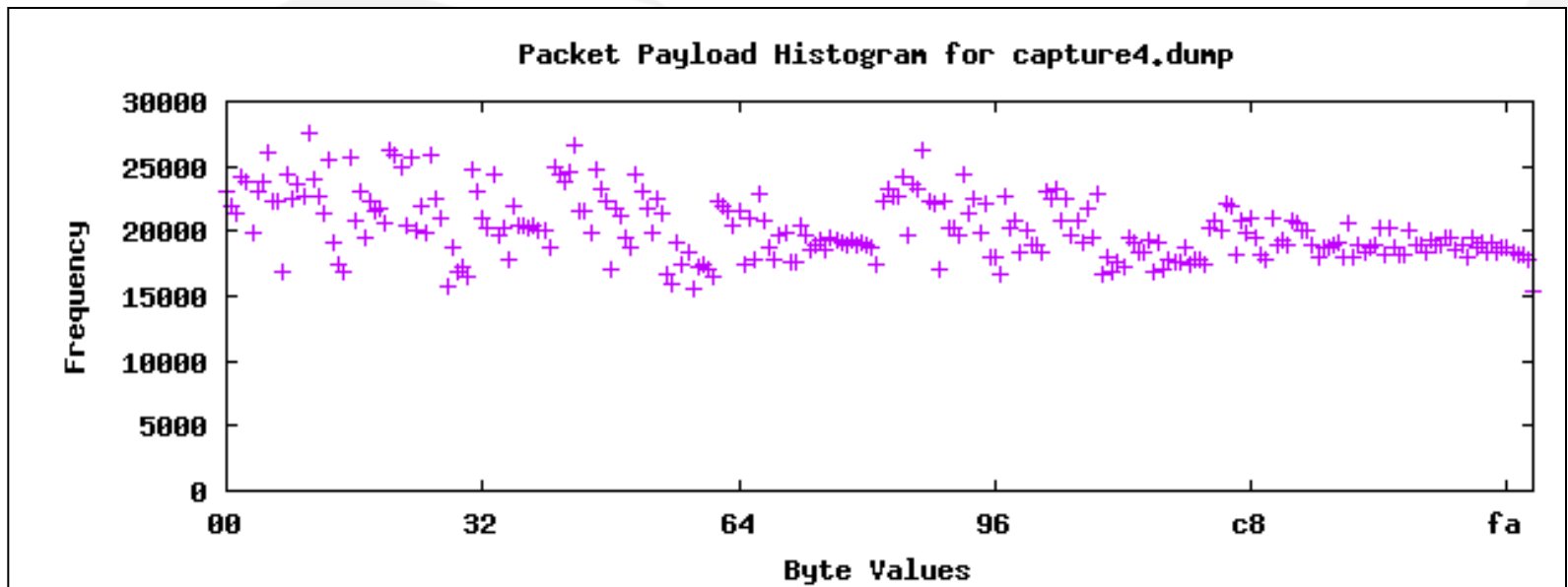
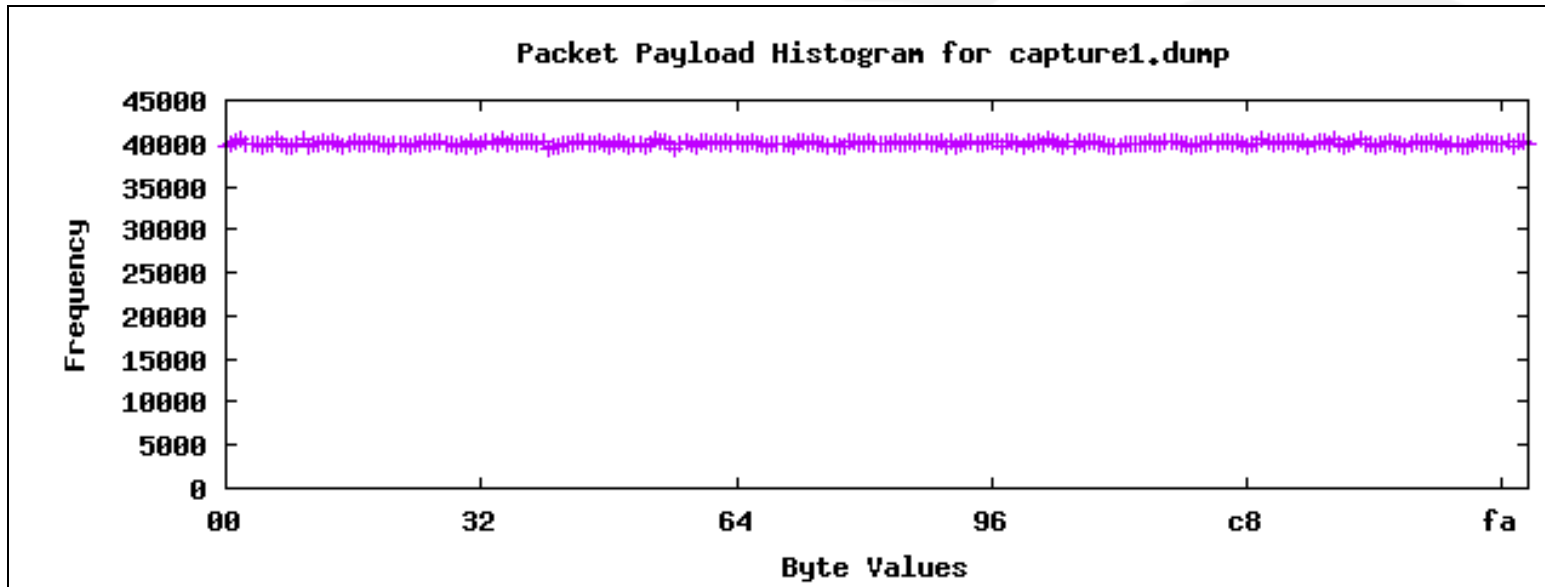


- These tasks include all network traffic between any of the devices regardless of its location such as sever-to-server, server-to-device, device-to-device
- RF Packet Analysis is not commonly performed because we assume all security is handled in the network protocols
 - Frequency hopping isn't a security control

Network Coms Pentest Tasks



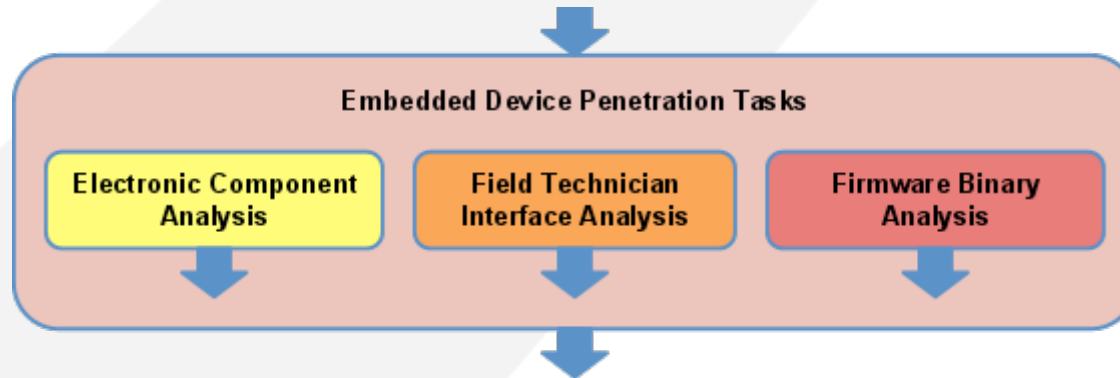
Task: Cryptographic Analysis



- AES ciphers using CTR mode effectively become a stream cipher
- Without key derivation and rotation, IV collisions compromise integrity of cipher

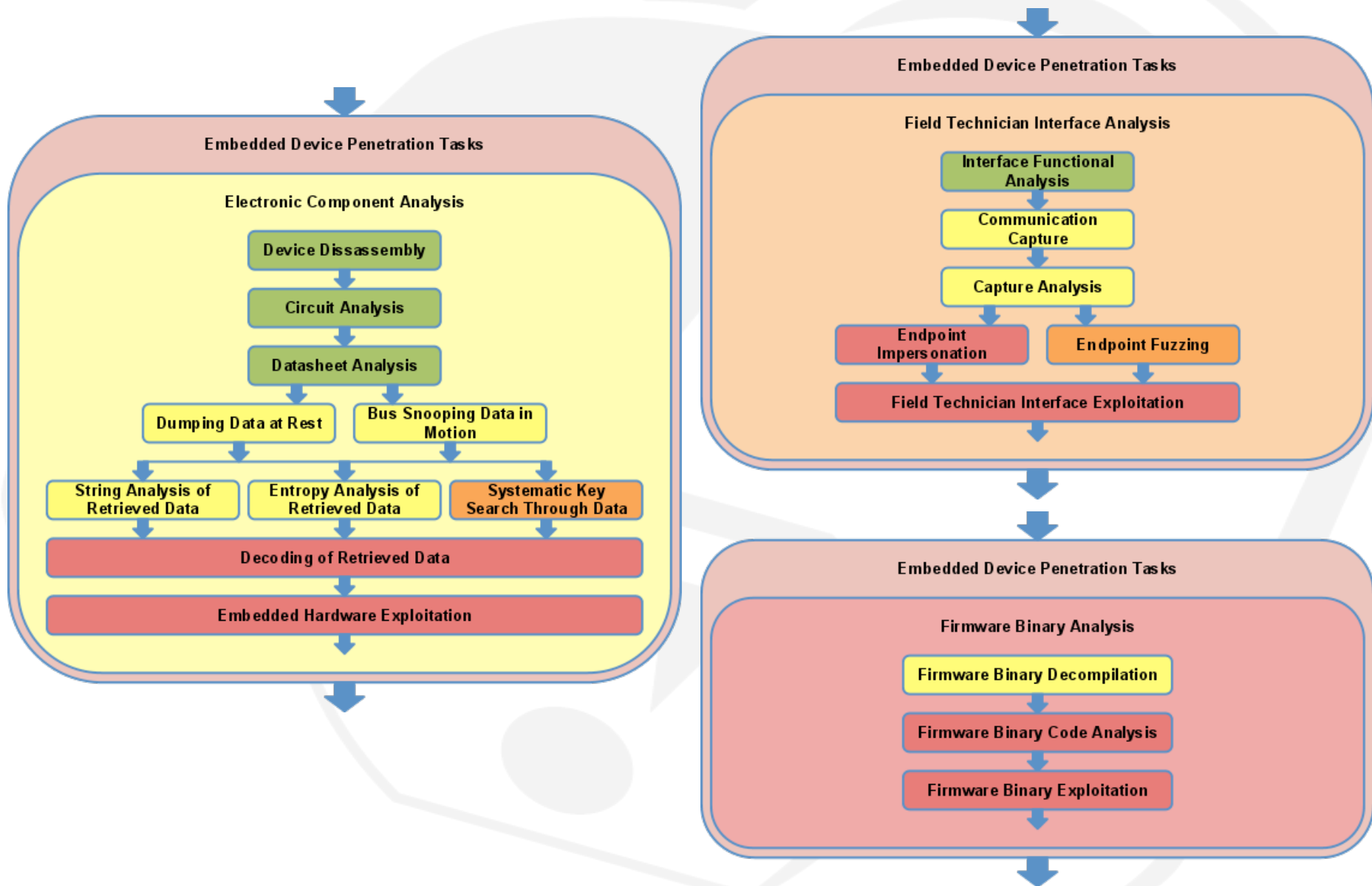
```
C:\>type ivcoltest.py
#!/usr/bin/env python
knownplain = "\xaa\xaa\x03\x00\x00\x00\x08\x00\x45\x00\x01\x48\x00\x01\x00\x00"
knowncip = "\x31\xb9\x84\x81\xe1\x96\x6e\x71\xd8\xa3\x39\x0c\xfb\x48\xaa\x61"
unknowncip = "\x31\xb9\x84\x81\xe1\x96\x6e\x71\xd8\xa3\x3d\x0c\xfb\xb5\xaa\x61"
print "Decrypted packet: "
for i in range(0,len(knownplain)):
    print "%02x"%( (ord(knownplain[i]) ^ ord(knowncip[i])) ^ ord(unknowncip[i]) ),
print("\n")

C:\>python ivcoltest.py
Decrypted packet:
aa aa 03 00 00 00 08 00 45 00 05 48 00 fc 00 00
```



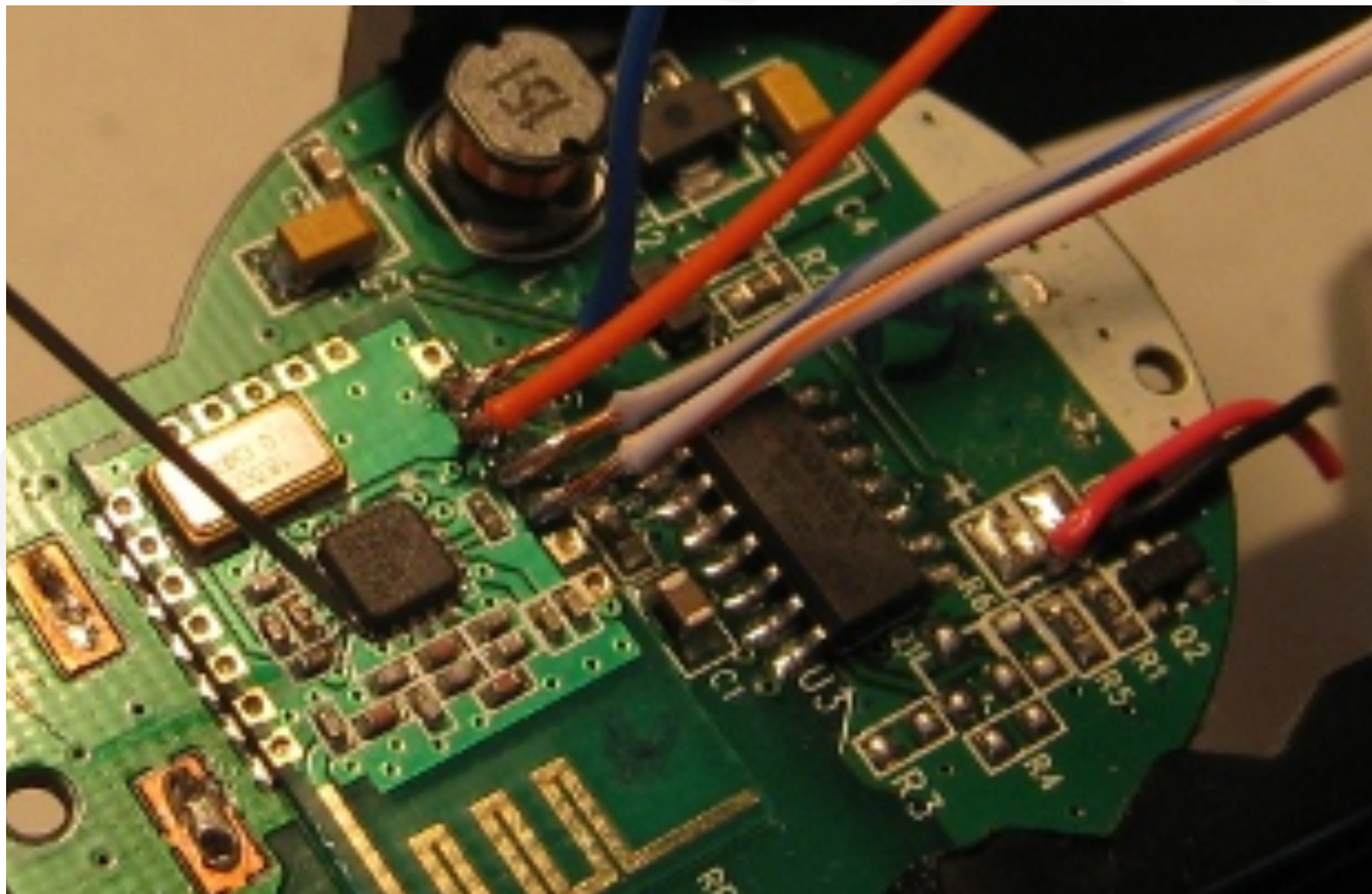
- These tasks target physical attacks on embedded field devices:
 - electronic components that store data (EEPROM, Flash, RAM, MCu storage)
 - buses that pass data between components (parallel buses and serial buses)
 - input interfaces used for administrative or debugging purposes (serial ports, parallel ports, infrared/optical ports)
- Overarching goal for embedded device testing is to identify vulnerabilities that allow attackers to expand their control of that single device to other devices with limited or no physical access to those other devices

Embedded Device Pentest Tasks

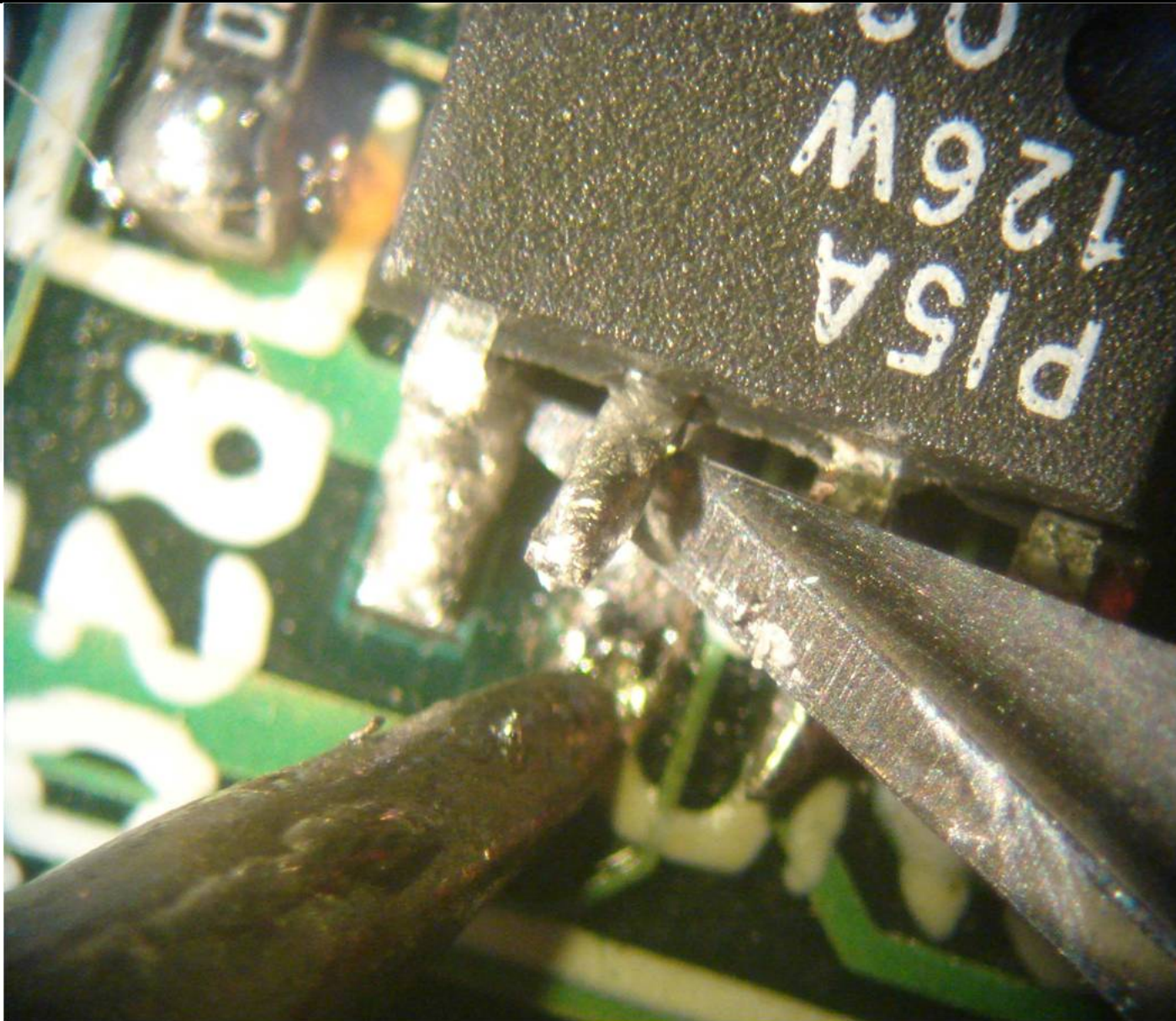


- Attacking data at rest
 - Power down the device, expose its circuit board, and interact directly with each component
 - Extract contents of accessible RAM, Flash, and EEPROM
 - Identify cryptography keys or firmware
- Attacking data in motion
 - Boot and normally operate the device in a lab, monitoring bus activity between major chips (MCU, Radio, Flash, RAM)
 - Crypto keys can often be found in key load operations between a microcontroller and crypto accelerator
 - Firmware can often be found in boot processes (between Flash and MCU) and firmware updates (between Radio, MCU, and Flash)

Interfacing with an IC



Lifting an IC's Chip Enable (CE) Pin UtiliSec





Task: Bus Snooping Data in Motion UtiliSec

The screenshot shows the 'spi-eeprom - Total Phase Data Center' application window. The main table displays a list of transactions with columns for Index, ms.ms.us, Dur, Len, Err, Record, and Data. The table shows multiple transactions, with the 4th transaction (Index 4) selected. The 'Data' column for this transaction shows a sequence of bytes: 0200 0000 0000 0000 0100 0200 0300 0400 0500 0600 0700 0800 ...

The 'Navigator' panel on the right shows the 'SPI Bus' section with two 'SPI Slave Device' entries. The 'SPI Slave Device' details panel shows the following settings:

SPI Slave Device	
Bit Order	MSB first
Sampling Edge	Rising edge
Slave Select Polarity	Active low

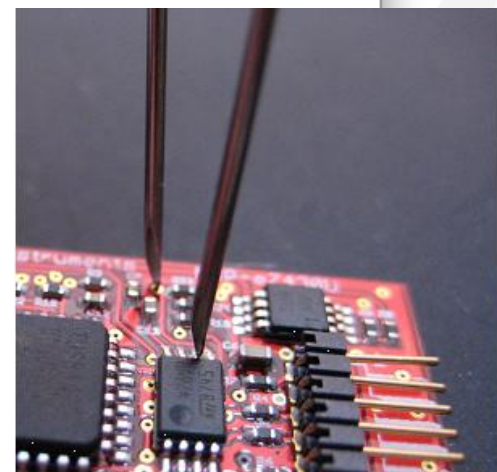
The 'Command Line' panel at the bottom left shows the following commands:

```
Action cancelled.  
2> example  
3> open(u'/Applications/Data Center.app/example/spi-eeprom.tdc')  
Buffer cleared.  
File opened.  
4> lens('spi')  
Filter disabled.  
Lens has been set to spi.
```

The 'Details' panel at the bottom right shows the 'MOSI Data' tab with the following data:

Offset	0	1	2	3	4	5	6	7	ASCII
00000	02	00	00	00	01	02	03	04	
00008	05	06	07	08	09	0A	0B	0C	
00010	0D	0E	0F	10	11	12	13	14	
00018	15	16	17	18	19	1A	1B	1C	
00020	1D	1E	1F						...
00028									
00030									
00038									

The 'Bus' panel at the bottom right shows the 'Bus' tab selected, with 'Filter' and 'Info' tabs also visible. The status bar at the bottom indicates 'Ready' and 'Disconnected'.



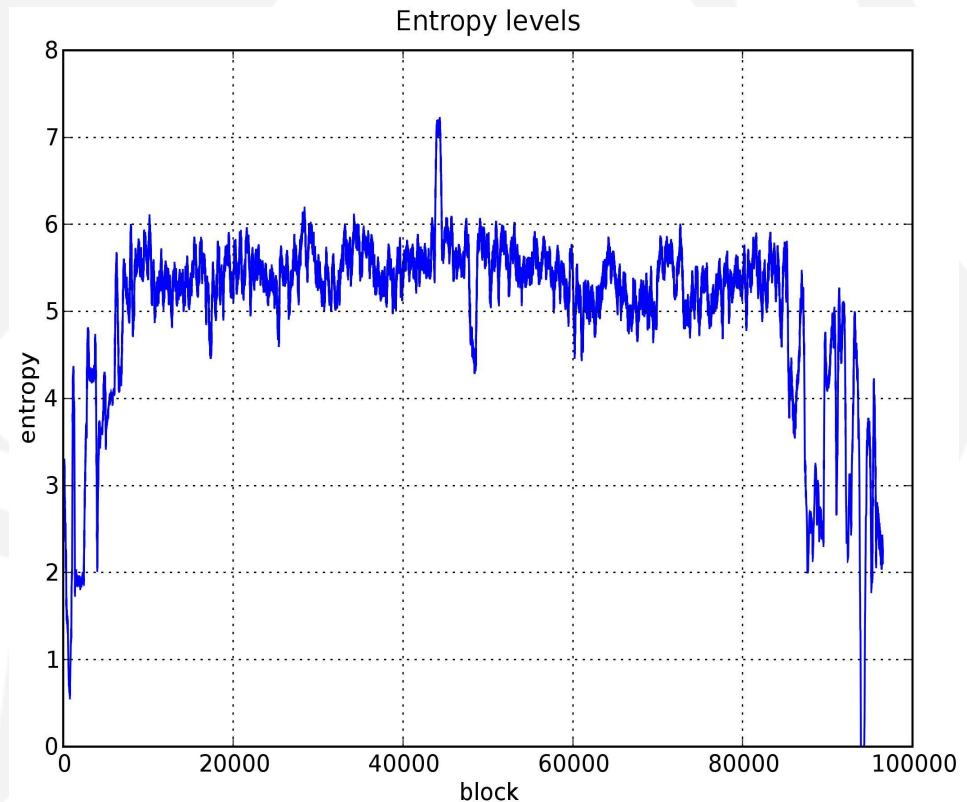
Task: Systematic Key Search

- Perform basic string searches for obvious keys
- Develop custom tools to do more advanced searches:
 - GoodFET: Abuses vulnerability in TI, Ember radios to access RAM even when chip is locked
 - zbgoodfind: Search for ZigBee key using RAM dump as a list of potential keys
 - Combined they can recover the ZigBee network key

```
$ sudo goodfet.cc dumpdata chipcon-2430-mem.hex
Target identifies as CC2430/r04.
Dumping data from e000 to ffff as chipcon-2430-mem.hex.
...
$ objcopy -I ihex -O binary chipcon-2430-mem.hex chipcon-2430-mem.bin
$ zbgoodfind -R encdata.dcf -f chipcon-2430-mem.hex
zbgoodfind: searching the contents of chipcon-2430-mem.hex for
encryption keys with the first encrypted packet in encdata.dcf.
Key found after 6397 guesses:  c0 c1 c2 c3 c4 c5 c6 c7 c8 c9 ca cb cc
cd ce cf
```

Task: Entropy Analysis of Data

- Asymmetric keys have high entropy (very random)
- RAM and Flash is filled with non-random data
- Graphing entropy of flash reveals a spike in randomness
- This spike is the location of the asymmetric key in flash



Demo



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