

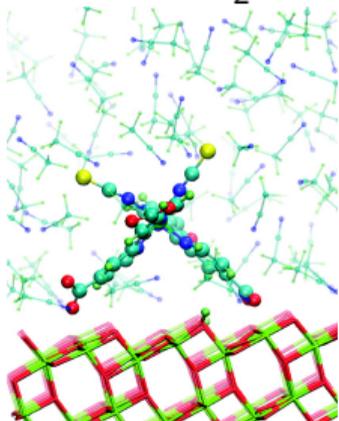
Accelerated Sparse Matrix Multiplication for Quantum Chemistry with CP2K on Hybrid Supercomputers

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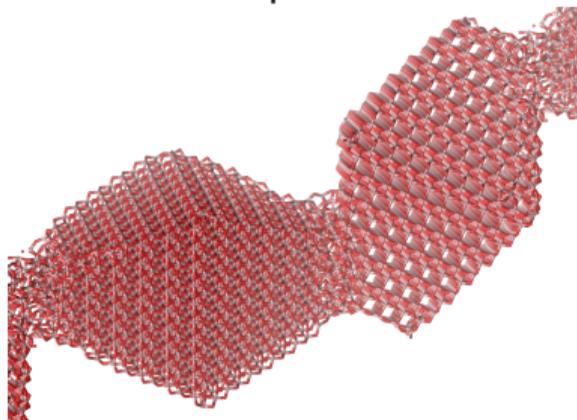
Nanoscale Simulations

Processes at TiO_2 -Interface



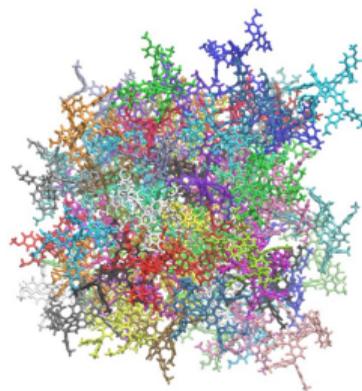
Schiffmann et al. (2010)

Electron Transport across Nanoparticles



17k atoms, 80k electrons

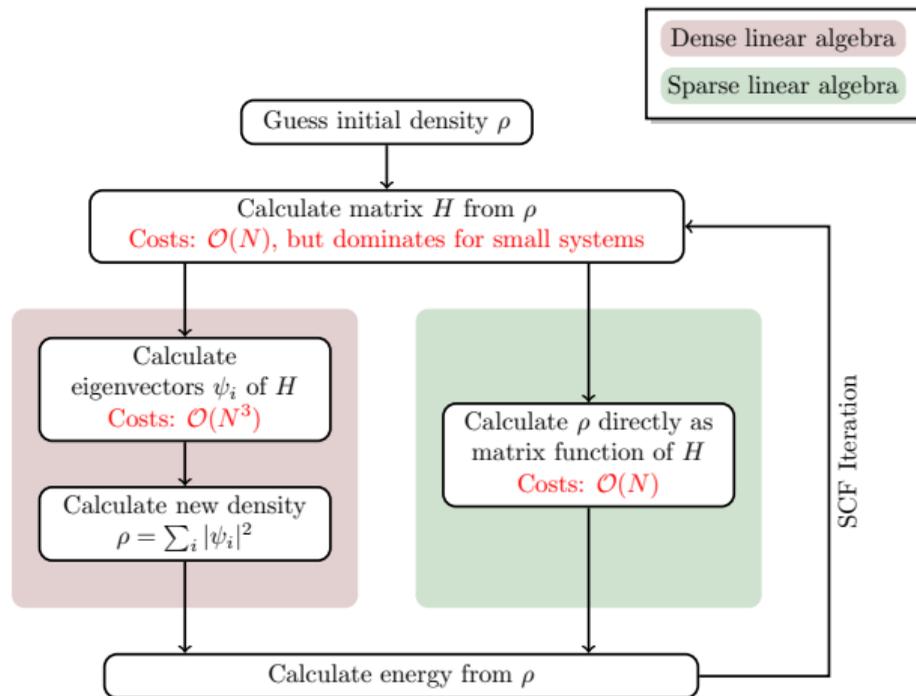
Hole Transporting Material (HTM)



spiro-MeOTAD

Requirements

- electronic properties \implies Schrödinger equation ($H\Psi = E\Psi$)
- lack of symmetries \implies large simulation cells (> 1000 atoms)



- Density P as matrix function of H :

$$P = \left[1 + \exp \left(\frac{H - \mu \mathbb{1}}{kT} \right) \right]^{-1}$$

$$= \frac{1}{2} [1 - \text{sign}(H - \mu \mathbb{1})]$$

↖ limit of small kT (ground state)

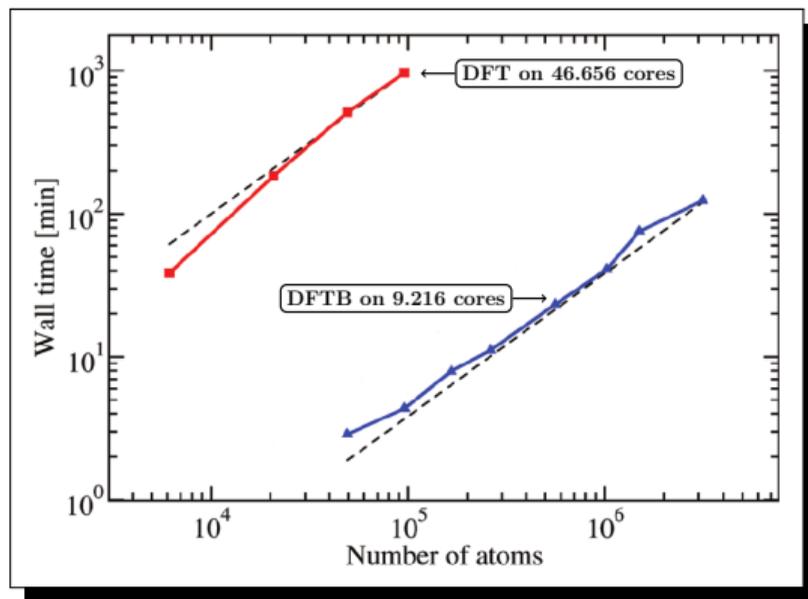
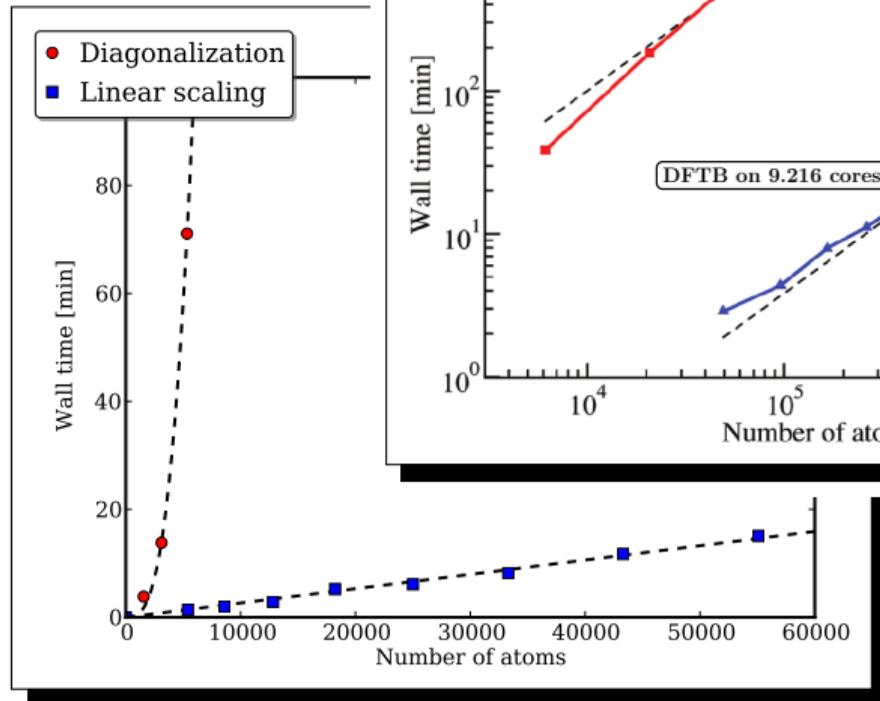
- Evaluate $\text{sign}()$ as polynomial series:

$$X_0 = A \cdot \|A\|^{-1}$$

$$X_{n+1} = \frac{1}{2} X_n (3\mathbb{1} - X_n^2)$$

$$\text{sign}(A) = X_\infty$$

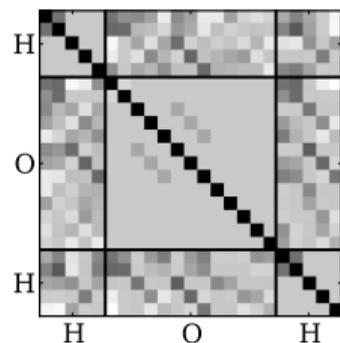
LS-SCF entirely based on sparse linear algebra.



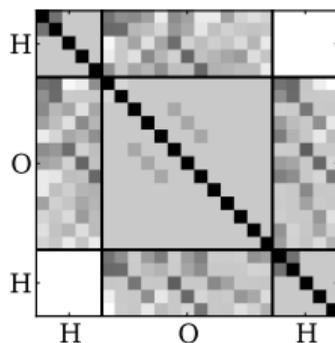
$O(N)$ methods
are inevitable
for large systems

VandeVondele et al. (2012):
*Linear Scaling Self-Consistent Field Calculations
with Millions of Atoms in the Condensed Phase*

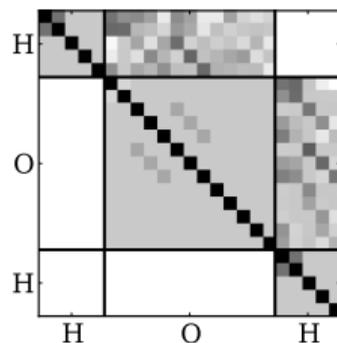
- DBCSR = Distributed Block Compressed Sparse Row
- Working horse of CP2K's linear scaling DFT code
- Non-zero elements are small dense blocks e.g. 13×13
- Each block corresponds to interaction between two atoms
- Additions are *local* operations
- Multiplications are more elaborate...

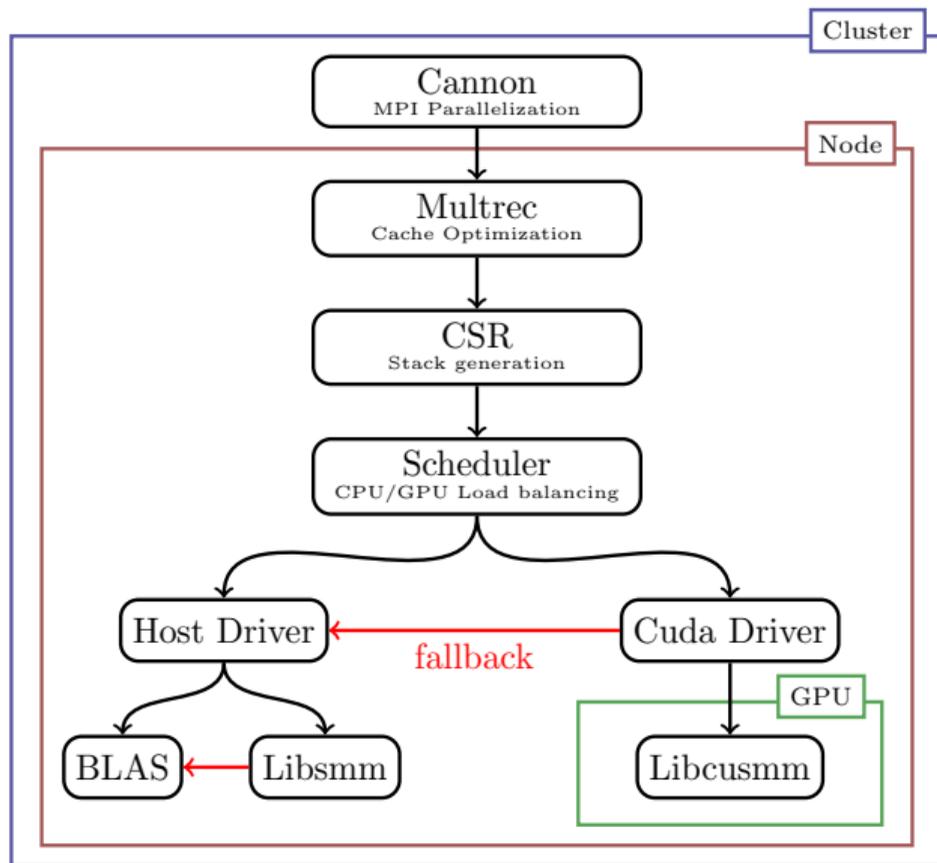


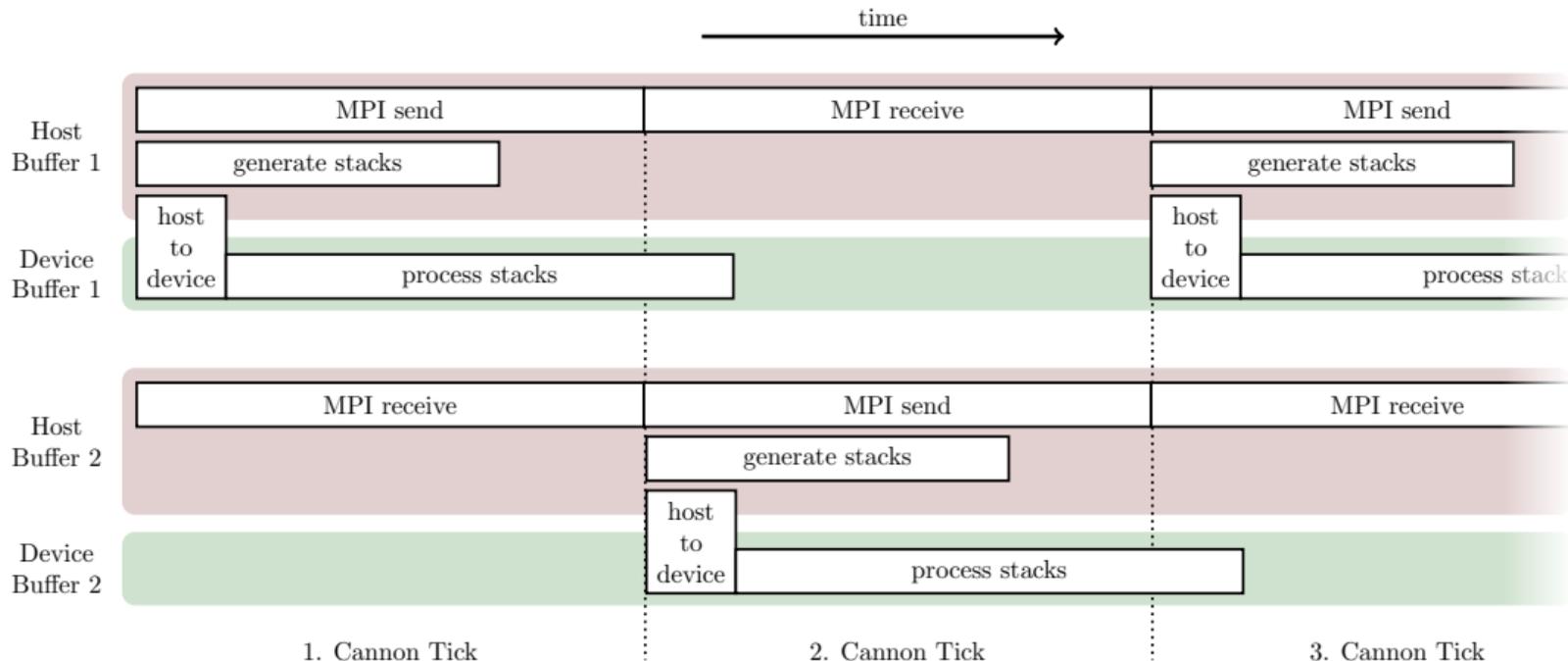
neglect
distant
atom pairs



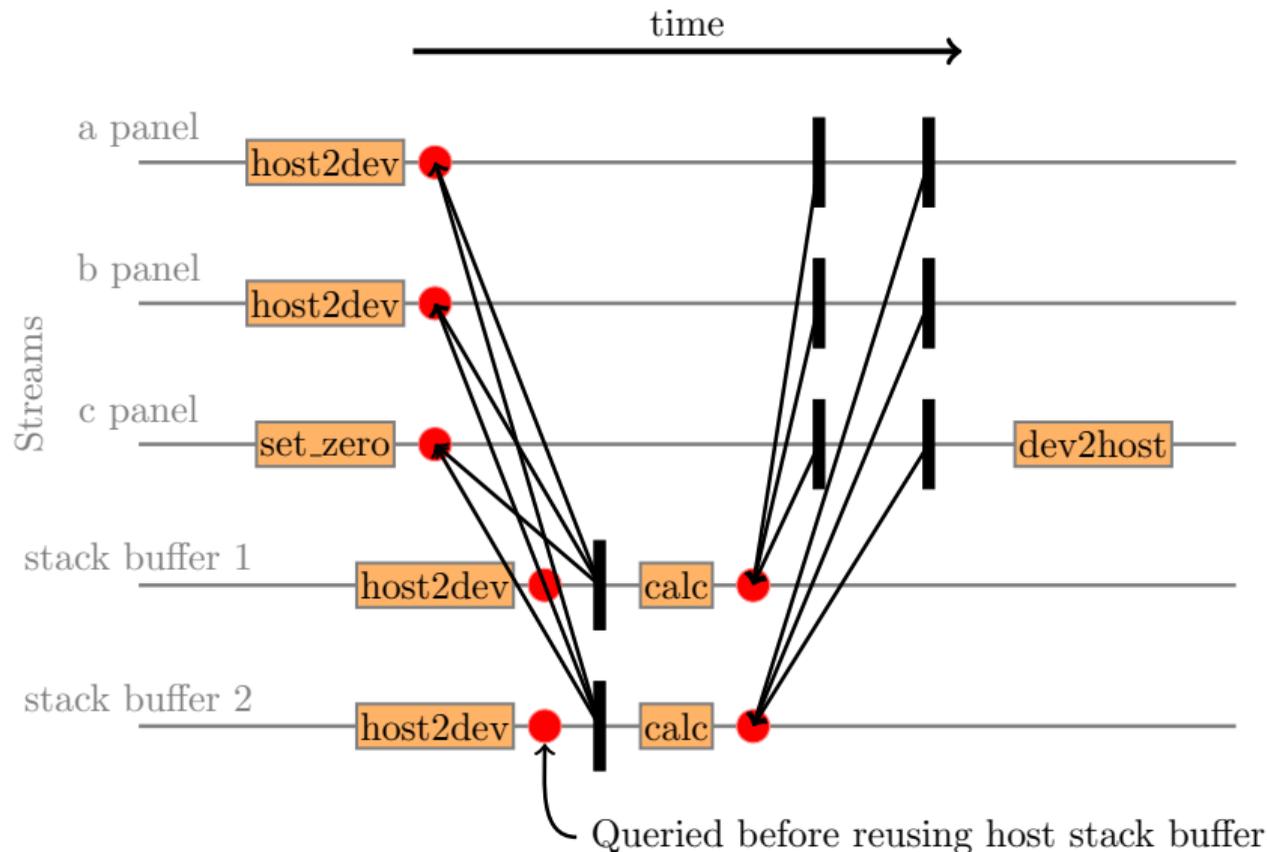
exploit
symmetry



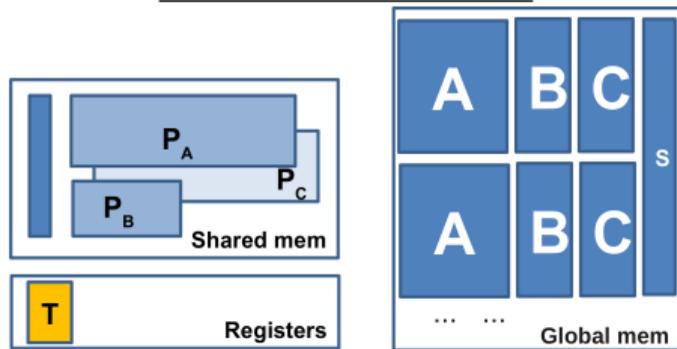




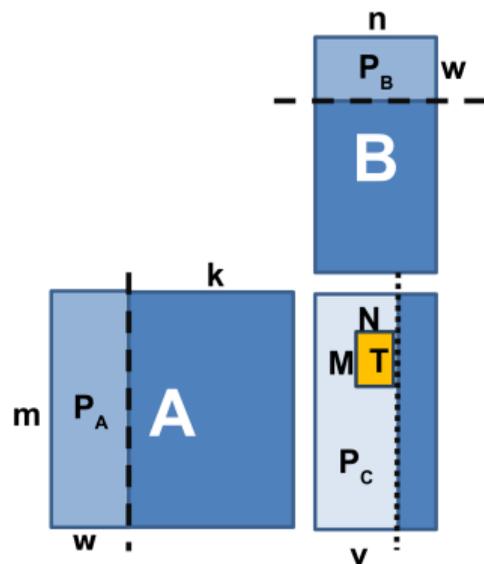
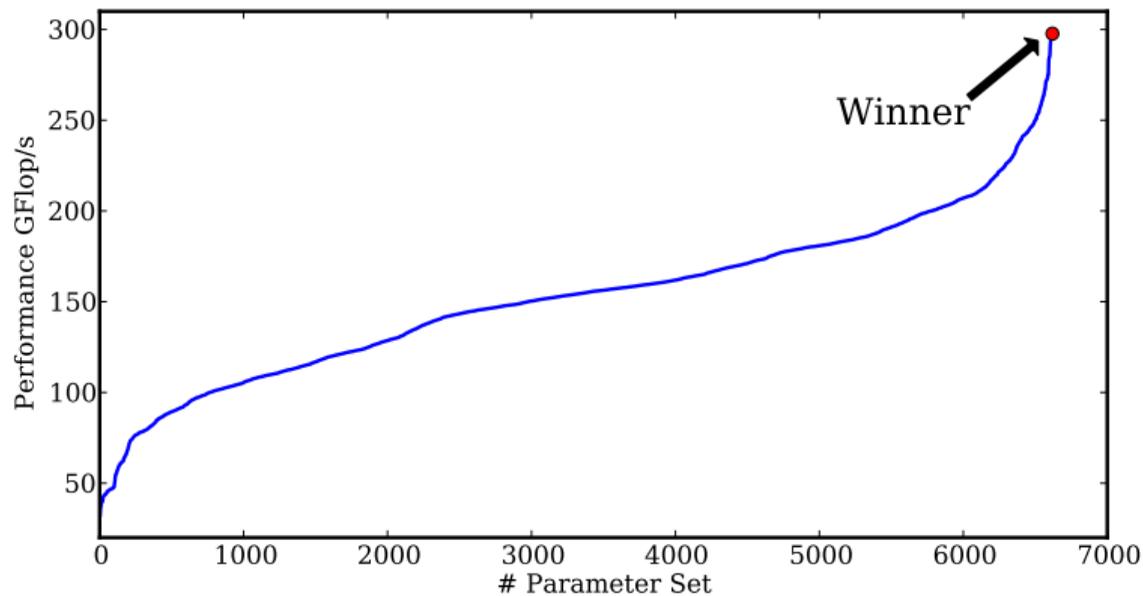
Ideally: Network and GPU always busy



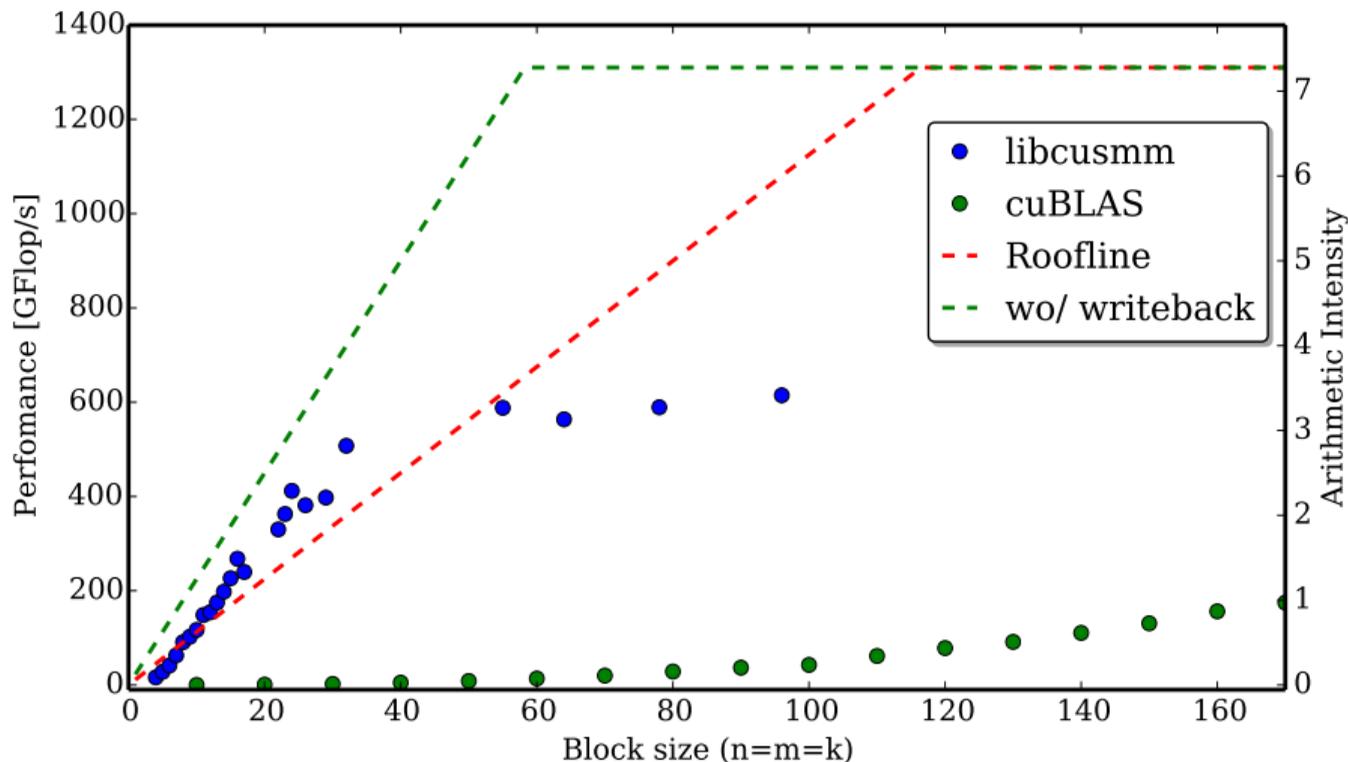
GPU Memory Usage



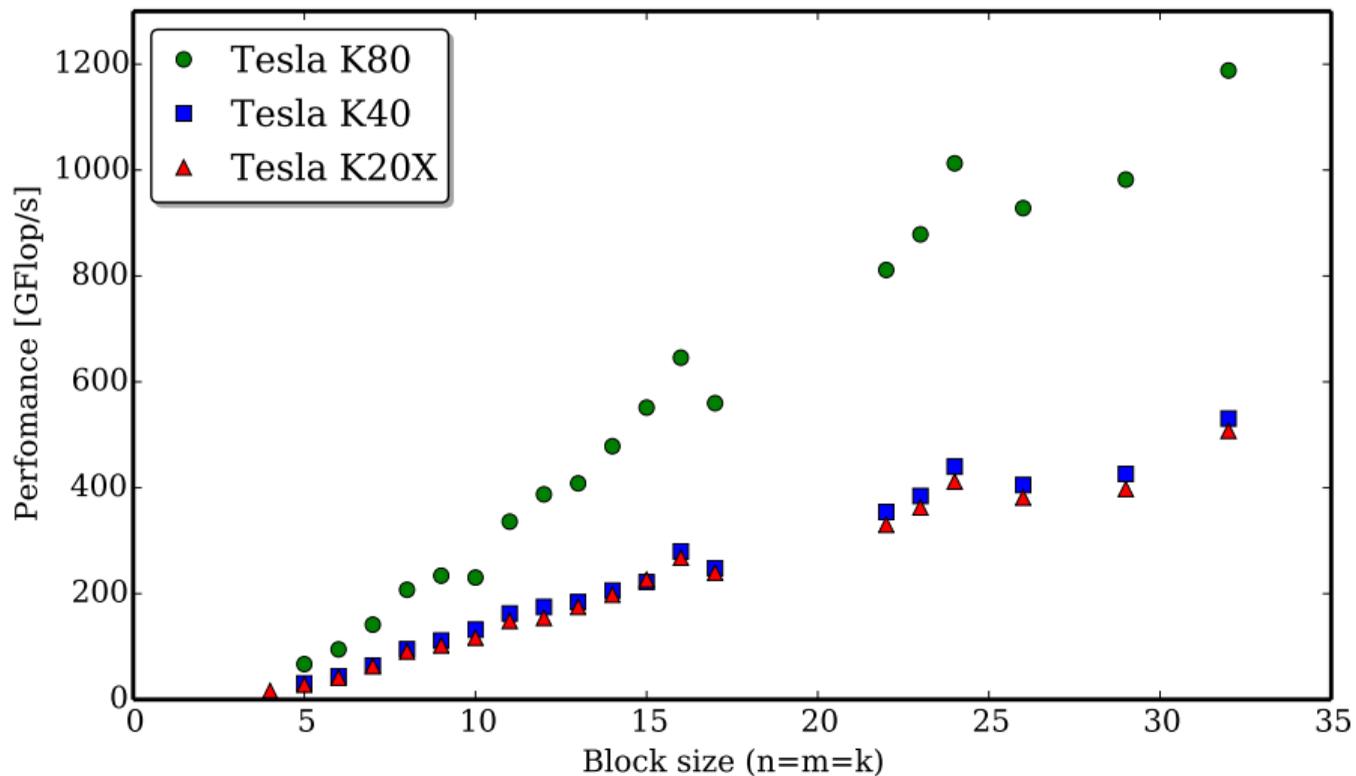
- Larger matrices are processed in slabs P_A , P_B , P_C
- Each thread computes a tile T of the result slab P_C
- Results T are kept in thread's registers
- Outer-product style multiplication reduces access to P_A and P_B
- P_B is stored transposed to coalesced memory access
- Write back to global memory uses *Compare-and-Swap*

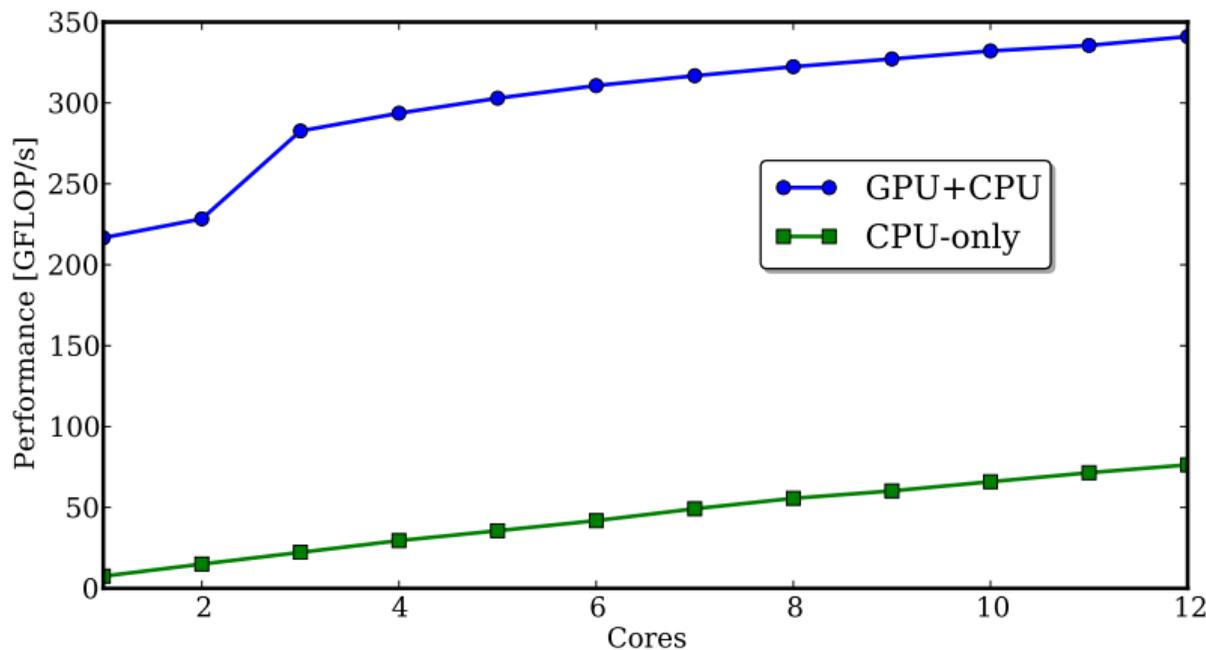


- Six parameters to optimize: v , w , N , M , $\#threads$, $\#minBlocksPerSM$
- On average > 8500 parameters-sets per kernel (heuristically pruned)
- Number of kernels optimized so far: 2349



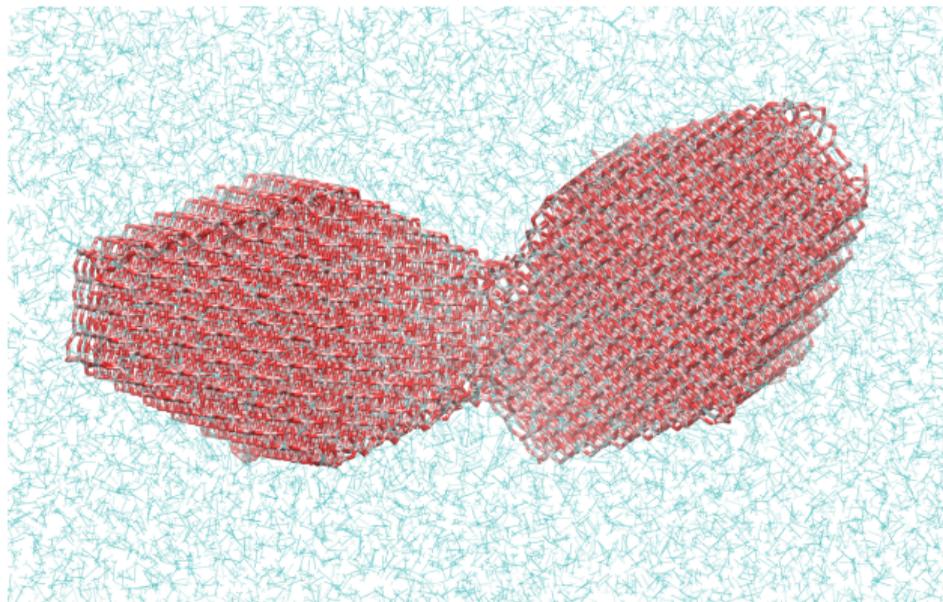
K20X GPU has 1.3TFlop/s and 180GB/s memory bandwidth with ECC





4.5x Speedup GPU+CPU vs CPU-only

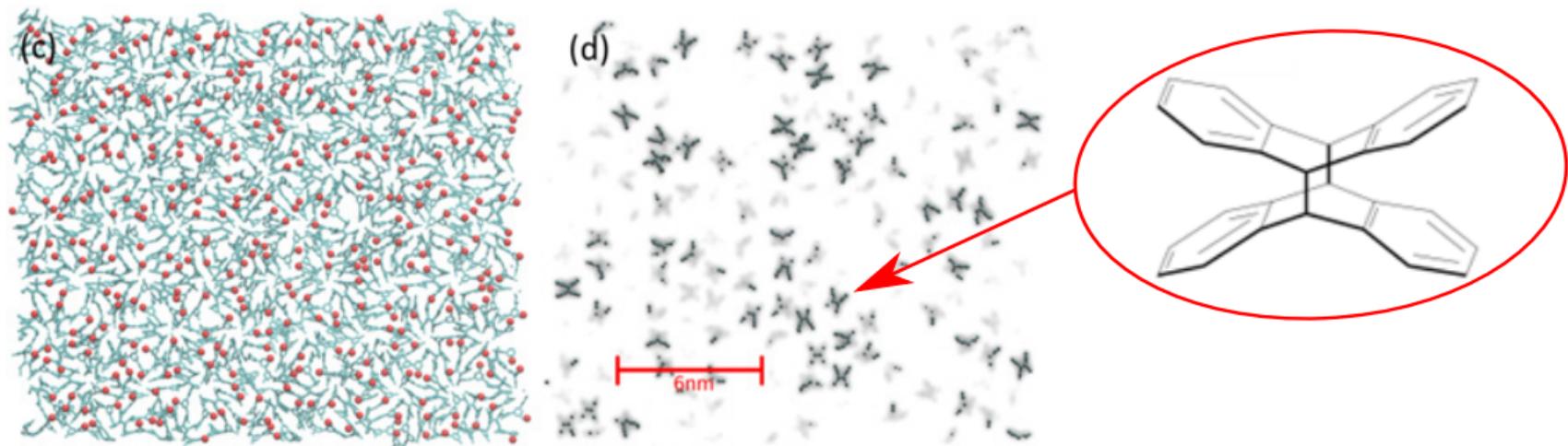
Artificial benchmark with favorable 23x23 block-size; Dual Sandy Bridge (E5-2620, 2.0GHz, 6 cores); Nvidia K20 GPU.



80'000 atoms DFT, high accuracy settings
Aggregated nanoparticles in explicit solution
Relevant for 3rd generation solar cells

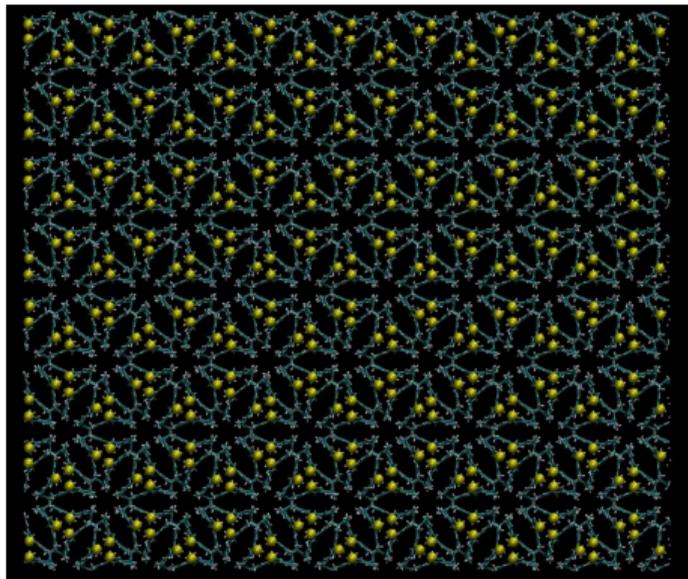
- Matrix dims: 772868×772868
- Filter threshold: 10^{-6}
- Matrix occupation $\approx 4\%$
- SCF steps ≈ 50
- # multiplies needed ≈ 2000
- Dense flops needed:
1846613343679824128000
- Actual flops needed:
849928403736295802
- Sparsity boost: $2172\times$
- GPU flop share: 99.4%
- Walltime on 5184 nodes: 6264s

2D polymers: synthetically tailored 2D materials beyond graphene



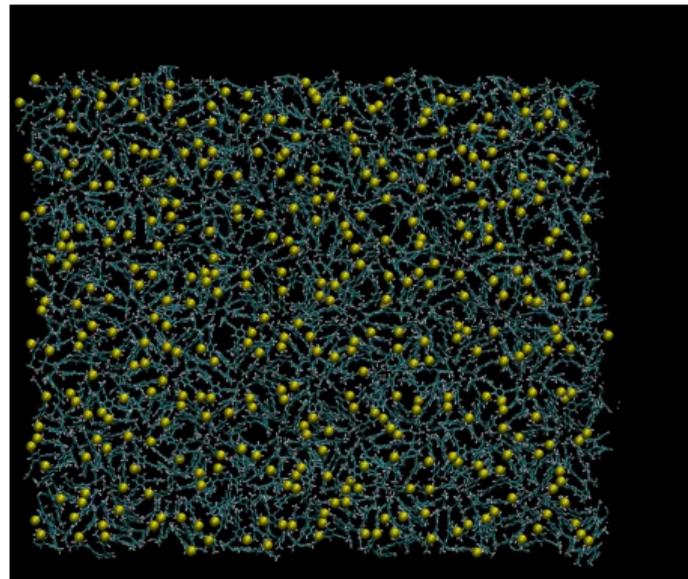
Based on linear scaling MD simulations for 10'000s of atoms, the morphology and properties of the proposed 2D polymer sheets has been investigated

Area: 223\AA^2

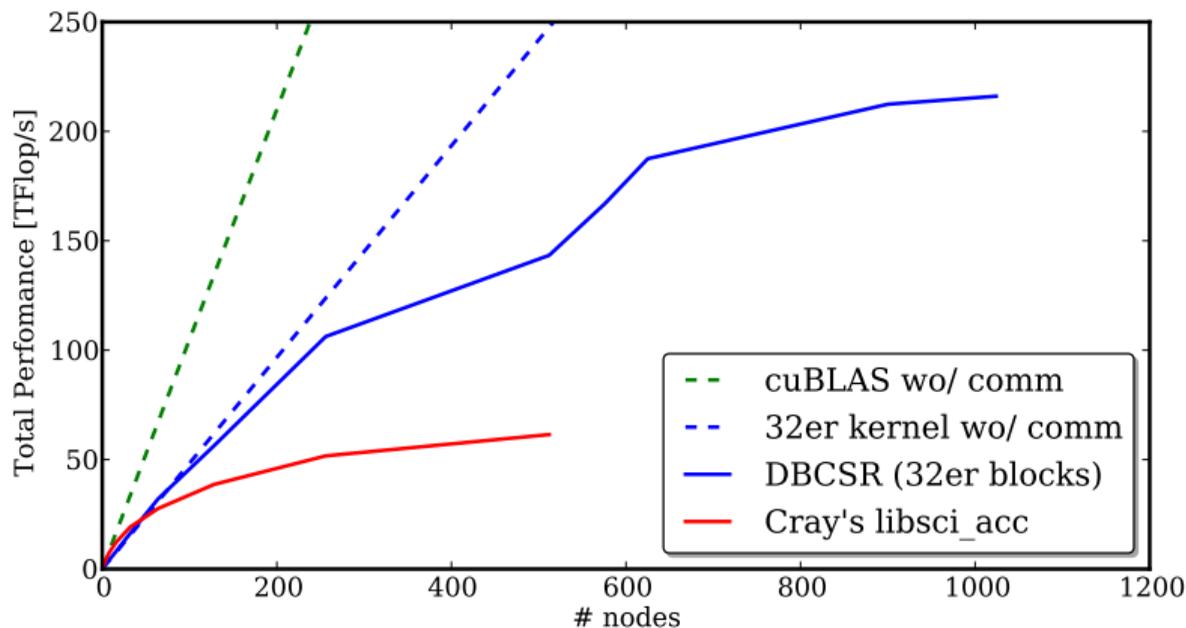


2ps of MD

Area: 168\AA^2



- Matrix Functions: Diagonalization \rightarrow Taylor series
- Matrix Inverse: Cholesky \rightarrow Hotteling



Benchmark of pdgemm, 32kx32k double precision matrix

Our DBCSR library enables $\mathcal{O}(N)$ quantum chemistry methods, which allow for novel science.

Lessons learned

- Overlapping communication with computation is key
- Auto-tuning is the way to go
- Avoid manual scheduling, use Cuda events

Acknowledgements

- Joost VandeVondele
- Florian Schiffmann
- Urban Borstnik
- Peter Messmer

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- <http://cp2k.org>

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