CTB directional gradient detection using 2D-DWT for Intra-picture prediction in HEVC

Presenter Information

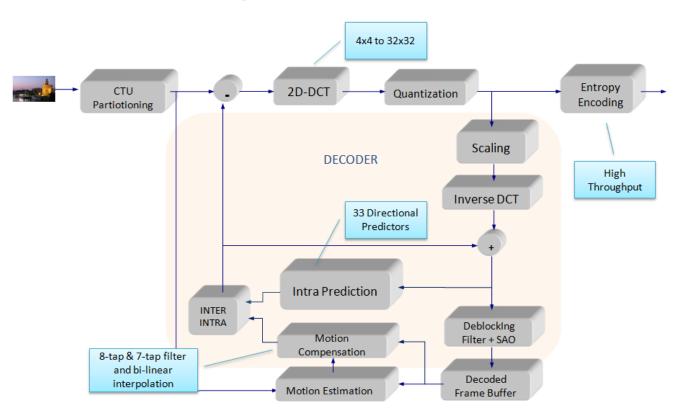
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Agenda

- Motivation
- Intra Prediction in HEVC
- Proposal of gradient detection using wavelet transform
- 2D-DWT implementation on GPU
- Simulation results
- Conclusions

HEVC block diagram



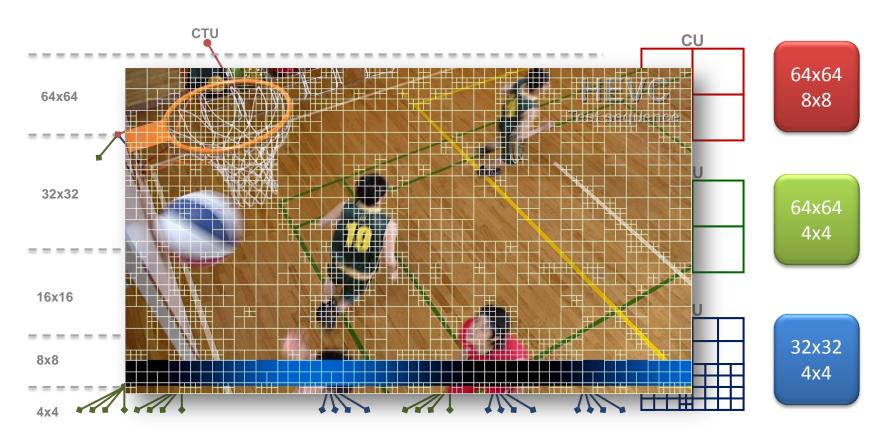
Motivation

- HEVC is the new video coding standard approved jointly by the ITU-T and ISO/MPEG, in January 2013
- HEVC allows a bit rate savings over 50% with respect to its predecessor, the H.264/AVC standard
- HEVC is expected to replace H.264/AVC especially for resolution formats beyond HD, UltraHD (4K and 8K)
- The new Intra-frame coding of HEVC outperforming the current state of the art in image coding, such as JPEG, JPEG2000 and JPEG XR
- However, these improvements are obtained at the expense of an increment in the encoder computational complexity

Motivation

- HEVC new hierarchical image partitioning, denoted as Coded Tree Block (CTB), with three new blocks units:
 - ✓ The Coding Units (CU) equivalent to Macroblock in H.264, in the range of 64x64 to 8x8.
 - ✓ The Prediction Units (PU) with sizes in the range of 64x64 to 4x4
 - ✓ The Transform Units (TU) with sizes from 32x32 to 4x4
- The decision of the optimal CTB partitioning is content depended, and it consumes most of the computational burden of HEVC encoder.

Motivation



Intra Prediction in HEVC

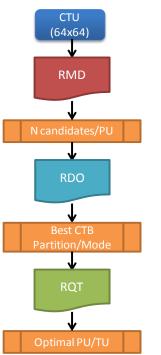
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TOTAL

The intra-prediction in HEVC is five time more complex th Mode Angle Slope Mode Angle Slope $Tg(\Theta_i)$ $Tg(\Theta_i)$ Θ_{i} (dy/dx) M_i Θ_{i} (dy/dx)H2 225° Increas H3 219.1° 26/32 129.1° -1.23-32/260.81 V19 H4 213.2° 0.65 21/32 V20 123.3° -1.52-32/21from 9 H5 207.9° 0.53 17/32 V21 118.0° -1.88 -32/17202.1° 112.1° **H6** 0.40 13/32 V22 -2.46-32/13H7 195.7° 0.28 9/32 V23 105.7° -3.55 -32/9Increas H8 188.9° 98.9° 0.15 5/32 V24 -6.4-32/5H9 2/32 V25 93.6° -32/2183.6° 0.06 16 (4x4 ar H10 180° 0 0 V26 90° ∞ V27 86.4° H11 176.4 -0.06-2/3216 32/2 81.1° H12 171.1° -0.15-5/32V28 6.4 32/5 H13 164.3° -0.28-9/32V29 74.3° 3.55 32/9 # PUs H14 157.9° -0.40-13/32V30 67.9° 2.46 32/13 64x64 H15 152.0° -0.53 -17/32V31 62.0° 1.88 32/17 32x32 H16 146.7° -0.65-21/32V32 56.7° 1.52 32/21 16x16 H17 140.9° -0.81-26/32V33 50.9° 1.23 32/26 8x8 64 4x4 256 135° V34 45° D18

Intra Prediction in HEVC

■ The HEVC reference model implements a three steps algorithm:



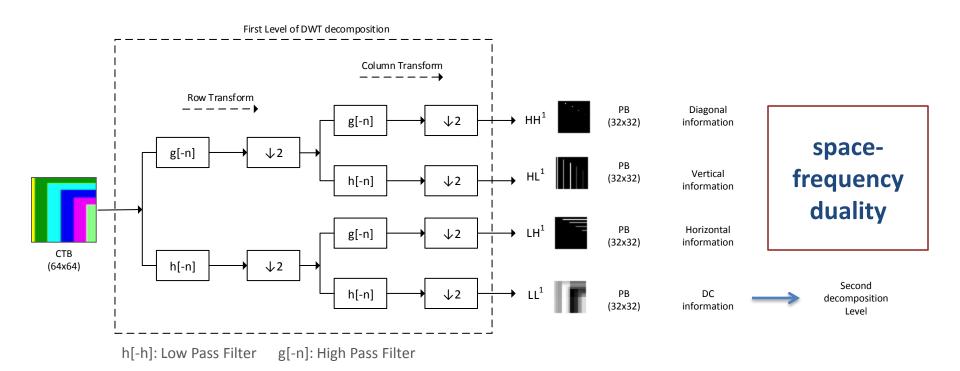
- 1- Rough Mode Decision: computes fast Hadamard cost
- ->Selection of 8 mode candidates for each PU size
- **2- Rate Distortion Optimization:** exhaustive cost of candidates
- ->Selection of Best Mode/PU size
- **3- Residual Quad Tree**: full search of TU depths for Mode/PU
- ->Selection of Best TU size/ (Mode/PU)

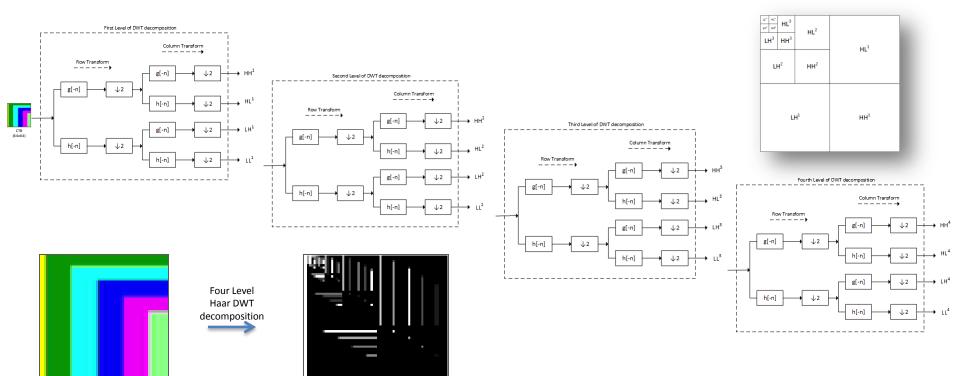
- The RMD evaluates the 35 prediction modes, for each of five PU sizes, and in sequential order
- Our proposal replaces the RMD stage by using a directional wavelet (directionlet) transform, which allows to detect the dominant gradient of the PU.
- Our approach can be computed in parallel, and is specially suitable for multi-core graphic processors.
- The algorithm reduces also the number of candidates modes from 8 (RMD) to 3 modes (four modes for diagonal direction)

Traditional Wavelet Transforms (WT) has been shown to be an efficient tool for edge detection and textures classification

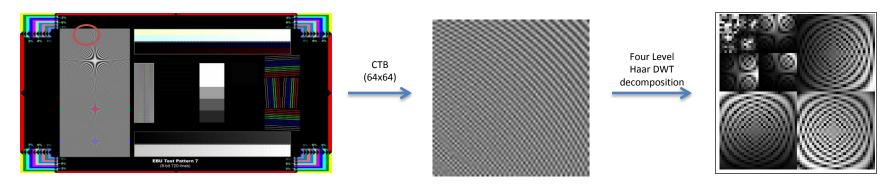
- Conventional 2D-Wavelets transform can be implemented as two separated 1D FIR filtering over the rows and columns of the image.
- The results are four sub-bands images achieving information of edge with Horizontal (LH¹), Vertical (HL¹), diagonal (HH¹) orientations, and DC (LL¹).
- The LL sub-band can be newly processed using 2D wavelet transform, achieving four sub-bands of the second decomposition level (LH²,HL², HH², LL²).

CTB (64x64) **EBU Test Pattern 7** (8-bit 720-lines)





Rows and column DWT provides poor information for gradients other than rows and columns transform directions (0°, 90°)

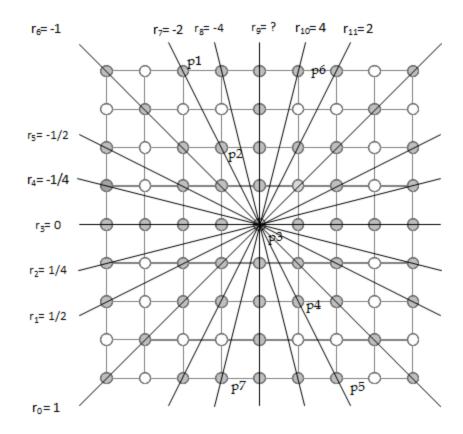


In order to estimate the 33 intra prediction directions in HEVC, we need to achieve a gradient information in additional directions.

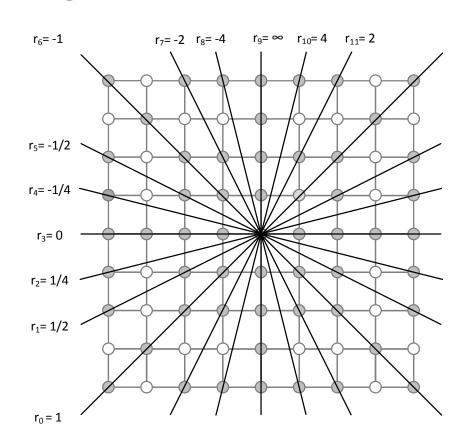
- We compute 10 additional 1D wavelet transform with rational directions (dy/dx)
- Example how to get the 10 additional 1D-DWT:
 - 1. 8x8 block is transformed into 10 lines each one of different length:

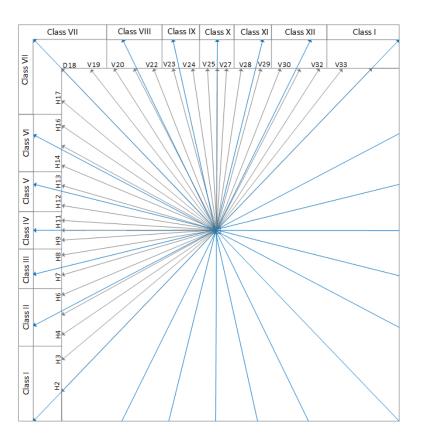
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- 2. Over this lines apply 1D-DWT get to sublines size N/2
- 3. Measure the energy of the coefficients to see in which direction we have less high frequencies indicating that this direction is more homogeneous



- The 10 directions were selected in a way so that all directions can be calculated for all PU sizes. Making sure that each line has at least two points so its well defined
- The conventional row and columns wavelets transform, in addition to this new directions, conform a set of 12 'r_i' gradient directions.
- But only three slopes match with the HEVC angular modes:
 - $r_3 = H10$
 - $r_6 = D18$
 - $r_9 = V26$





We define 12 Classes (C_i), defining a set of candidate modes to each Class according to directional transform with slope 'r_i'.

Class	Transform Direction	Slope (dy/dx)	ф	Candidate modes
I	r_0	1/1	225°	H2, H3, V33, V34
II	\mathbf{r}_1	1/2	206.6°	H4, H5, H6
III	\mathbf{r}_2	1/4	194°	H7, H8
IV	\mathbf{r}_3	0	180°	H9, H10, H11
V	\mathbf{r}_4	-1/4	166°	H12, H13
VI	\mathbf{r}_{5}	-1/2	153.4°	H14, H15, H16
VII	r_6	-1/1	135°	H17, D18, V19
VIII	\mathbf{r}_7	-2/1	116.6°	V20, V21, V22
IX	r_8	-4/1	104°	V23, V24
X	\mathbf{r}_9	∞	90°	V25, V26, V27
XI	r_{10}	4/1	76°	V28, V29
XII	\mathbf{r}_{11}	2/1	63.4°	V30, V31, V32

- Each wavelet coefficient C^{λ}_{ϕ} (i,j) in level λ and direction ϕ , provides frecuencial information related to co-located (2 $^{\lambda}$ x 2 $^{\lambda}$) pixels of CTB
 - \circ Coeficients of $\lambda=2$ contain gradient information of PB_{4x4}
 - \circ Coeficients of $\lambda=3$ contain gradient information of PB_{8x8}
 - \circ Coeficients of $\lambda=4$ contain gradient information of PB_{16x16}
 - \circ Coeficients of $\lambda=5$ contain gradient information of PB_{32x32}
 - \circ Coeficients of $\lambda=6$ contain gradient information of PB_{64x64}

For each PU_{NxN} the dominant gradient ϕ is selected as the maximum C^{λ}_{ϕ} (i,j) at level $\lambda = \log_2 N$

implementation on GPU

1D-DWT Transform we use the implementation provided by the SDK.

1 thread computes the average and details for one thread of adjacent values

2D-DWT Transform we use the implementation provided by our SCU colleague Ed Karrels. He will explain this implementation on 3/20 at 13:30 room LL21A

Apply 1D-DWT to each row

Transpose Image

Apply 1D-DWT now to each column

Transpose the image to recover initial layout

The implementation combines the DWT and transpose steps using shared memory to ensure all read/writes are coalescent

implementation on GPU

```
get 1 frame
Repeat 6 times
        1 kernel
           tile image in blocks of size NxN (N starts at 64)
           Apply the 2D-DWT transform
           calculate energy of coefficients getting the H, V and D
direction
        2 kernel
           reads the block creating the 10 lines and pass this as an
input to the 1D-DWT transform
           calculate energy of the 10 direction
        select the gradient for the NxN PU as the one with less high
Frequency components of the 12
```

Simulation results

- We ha used two 4K sequences (Class A) and five HD sequences (Class B), recommended by the JCT-VC, according to:
 - JCT-VC, "Common test conditions and software reference configurations," Join collaborative Team on video Coding 9th meeting, Doc. JCTVC-I1100, 2012.

Class	Sequence	Frames	Horiz. Resolut.	Vertic. Resolut.	Number CTBs
Class A	Traffic PeopleOnStreet	150	2560	1600	1000
Class B	BasketballDrive BQTerrace Cactus Kimono ParkScene	500 600 500 240 240	1920	1080	578

Processing a total of 1.502.240 CTBs

Simulation results of HEVC implementation

- ✓ PC with an Intel Core i7-2600 3.40 GHz processor, with 8GB RAM
- ✓ V. Studio 2013, and W7 O.S.
- ✓ Nvidia GTX 690 4096MB, 3072 CUDA cores

✓ Computed the average RMD time consumed by CTB, frame and

sequence

	Sequence	Frames	RMD/CTB (ms)	RMD/Frame (sec)	RMD/Sequence (sec)
	Traffic	150	6.053	6.047	907.050
A	PeopleOnStreet	150	6.096	6.090	913.500
В	BasketballDrive	500	6.116	3.113	1556.560
	BQTerrace	600	6.083	3.096	1857.630
	Cactus	500	6.200	3.156	1578.120
	Kimono	240	5.961	3.034	728.160
	ParkScene	240	5.972	3.046	729.670

Time consuming of RMD in HM implementation

≈ 6ms/CTB

Our Implementation in GPU reach an average speedup of 30%

Conclusions

Presented a new approach for accelerating the CU angular decision of the HEVC intra prediction step based on the DWT transform

The approach is scalable and suitable for GPU architectures

Can be integrated with other algorithms

Reduces the time compared with the reference HM standard model

Thank you for your attention

Questions?

Presenter Information (Name & Organization)