CATEGORY: DEVELOPER - PROGRAMMING LANGUAGES - DL03 CONTACT NAME POSTER Anton Wijs: A.J.Wijs@tue.nl P5185

Harnessing the power of GPUs for model checking

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On-The-Fly State Space Exploration

Construct a state space, given a model of a concurrent system [3] Model = set of interacting finite-state Labelled Transition Systems

New hash-table design for GPUs, with fine-grained parallelism Elements are placed in buckets using *warp-the-line* technique

Threads work in groups to generate state successors Parallelism at state-level

Block-local shared memory used for state caches Local duplicate detection reduces global hash table access

Work forwarding per block from one search iteration to the next Speeds up fetching new work for the next iteration



10-100x speedup



in Proceedings of the 19th International SPIN Workshop on Model Checking of Software (SPIN'12), volume 7385 of LNCS, pp. 98-116 (2012)

joint work with



in Proceedings of the 20th International Conference on Tools and Algorithms for the Construction and Analysis of Systems (TACAS'14), volume 8413 of LNCS, pp. 233-247 (2014)

GPU TECHNOLOGY CONFERENCE

Dragan Bošnački **Eindhoven University of Technology**

Stefan Edelkamp & Damian Sulewski University of Bremen

Probability Computations

Perform numerical computations for probabilistic model checking [1, 4] Needed to check if a probabilistic property holds in a discrete or continuous time Markov Chain

Solving systems of linear equations and performing matrix-vector multiplication Parallel matrix-vector multiplication used in Jacobi method for solving equation systems

Parallel termination checking achieves significant speedup Fast checking if next iteration is needed

Novel restructuring of input ensures coalesced memory access by threads Faster reading of input reduces multiplication run time up to four times

States / transitions are grouped in segments of 16 and 32 states Coincides with a half and a full warp of threads

20-35x speedup

Tools available at <u>http://www.win.tue.nl/~awijs</u>

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