CATEGORY: COMPUTATIONAL PHYSICS - CP23 CONTACT NAME

P5308

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GPUTECHNOLOGY CONFERENCE

Implementation on single GPU





Caveat: Update step parallel over *images*, suggests multi-GPU treatment. Stay tuned...

Results

Example calculation: annealing of disclination defect for cylinders in cylindrical confinement. 2 annealing pathways found: symmetric and asymmetric.

T. Iwama, N. Laachi, K. T. Delaney, B. Kim, M. Carilli, G. H. Fredrickson, Macromolecules 2015, submitted

Current work: MPI-GPU Finite Temperature String



Strong scaling attained around 100 thermal average steps per reinterpolate step

(Scaling data collected on the TACC's Maverick)

